

REFERENCES

1. FUNCTIONAL LOGIC DIAGRAM INDEX AND SYMBOLS	5653D74-1	495871
2. FUNCTIONAL LOGIC DIAGRAM REACTOR TRIP SIGNALS	5653D74-2	495872
3. FUNCTIONAL LOGIC DIAGRAM NUCLEAR INSTR AND MANUAL TRIP SIGNALS	5653D74-3	495873
4. FUNCTIONAL LOGIC DIAGRAM NUCLEAR INSTR PERMISSIVES AND BLOCKS	5653D74-4	495874
5. FUNCTIONAL LOGIC DIAGRAM PRIMARY COOLANT SYSTEM TRIP SIGNALS	5653D74-5	495875
6. FUNCTIONAL LOGIC DIAGRAM PRESSURIZER TRIP SIGNALS	5653D74-6	495876
7. FUNCTIONAL LOGIC DIAGRAM STEAM GENERATOR TRIP SIGNALS	5653D74-7	495877
8. FUNCTIONAL LOGIC DIAGRAM SAFEGUARDS ACTUATION SIGNALS	5653D74-8	495878
9. FUNCTIONAL LOGIC DIAGRAM ROD CONTROLS AND ROD BLOCKS	5653D74-9	495879
10. FUNCTIONAL LOGIC DIAGRAM STEAM DUMP CONTROL	5653D74-10	495880
11. FUNCTIONAL LOGIC DIAGRAM PRESSURIZER PRESSURE AND LEVEL CONTROL	5653D74-11	495881
12. FUNCTIONAL LOGIC DIAGRAM PRESSURIZER HEATER CONTROL	5653D74-12	495882
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16. FUNCTIONAL LOGIC DIAGRAM TURBINE TRIPS, RUNBACKS & SIGNALS	5653D74-16	495886
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18. FUNCTIONAL LOGIC DIAGRAM SEISMIC TRIP	8759D77	495888
19. FUNCTIONAL LOGIC DIAGRAM DIGITAL FW CONT SYS INPUT SIGNAL VALIDATION	5653D74-18	495889
20. FUNCTIONAL LOGIC DIAGRAM DIGITAL FW CONT SYS FW FLOW CONTROLLER & C _v DEMAND	5653D74-19	495890
21. FUNCTIONAL LOGIC DIAGRAM DIGITAL FW CONT SYS CONT VCV SEQ & TRACKING LOGIC	5653D74-20	495891
22. FUNCTIONAL LOGIC DIAGRAM DIGITAL FW CONT SYS SIGNAL SELECTOR LOGIC	5653D74-21	495892
23. DRAWING INDEX SOLID STATE PROTECTION SYS INTERCONNECTION & SCHEM. DIAGRAM	108D442-1	458862

WE DWG PG&E DWG

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5653D74-19	495890
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NOTES:

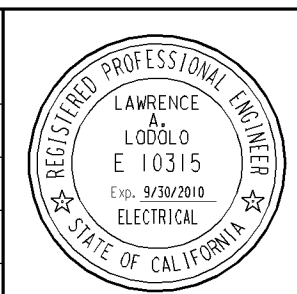
- SET-POINT FOR UNDERVOLTAGE RELAYS SHOULD BE APPROX. 70%. THE MAXIMUM ALLOWABLE DETECTOR TIME DELAY (WITH THE ADJUSTABLE DELAY SET TO ZERO) BETWEEN REACHING THE SETPOINT VALUE AND PASSING ON THE SIGNAL TO THE REACTOR TRIP CIRCUITRY SHOULD NOT EXCEED 0.6 SEC.
- REACTOR COOLANT PUMPS 1 & 2 ARE ON BUS 1. REACTOR COOLANT PUMPS 3 & 4 ARE ON BUS 2.
- THE SETPOINT OF THE UNDERFREQUENCY RELAYS SHOULD BE ADJUSTABLE BETWEEN 52 CPS AND 59 CPS. THE MAXIMUM ALLOWABLE DETECTOR TIME DELAY (INCLUDING THE ADJUSTABLE DELAY) BETWEEN REACHING THE SET-POINT VALUE AND PASSING ON THE SIGNAL TO THE TRIP CIRCUITS SHOULD NOT EXCEED APPROX. 0.1 SEC.
- THE MAXIMUM ALLOWABLE TIME DELAY BETWEEN THE TIME THE RCP BREAKERS RECEIVE A TRIP SIGNAL AND THE TIME THE BREAKERS HAVE TRIPPED AND PASSED ON THE OPEN SIGNAL TO THE REACTOR TRIP LOGIC SHOULD NOT EXCEED 0.1 SEC.
- SHEET NUMBERS REFER TO THE REFERENCE NUMBERS BELOW.
- WHENEVER A PROCESS SIGNAL IS USED FOR CONTROL AND IS DERIVED FROM A PROTECTION CHANNEL, ISOLATION MUST BE PROVIDED.
- THIS DRAWING ILLUSTRATES THE FUNCTIONAL REQUIREMENTS OF THE REACTOR CONTROL AND PROTECTION SYSTEM. THIS DRAWING DOES NOT REPRESENT ACTUAL HARDWARE IMPLEMENTATION. FOR HARDWARE IMPLEMENTATION, REFER TO THE APPLICABLE SCHEMATIC.

NUCLEAR SAFETY RELATED

KEY DWG, SECTION 3

UNIT 2

DATE 09-14-2009	REVISION DESCRIPTION REVISED PER DDN-2*117	DWG SCALE: BILL OF MATL: SUPDS51-663195-19
D.D. RxC2		SUPSD B13
R.E. FxC2		DRAWING SHEET PAGE REV 495875 1 0 5
I.V.		
P.E. LAL3		



PACIFIC GAS AND ELECTRIC COMPANY
SAN FRANCISCO, CALIFORNIA

RASTER=495875.dgn
DGN=495875.dgn
CAD User=RWGE Date=09-14-2009

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