

GENERAL NOTES:

- 1. THIS DRAWING IDENTIFIES OVERCURRENT PROTECTION DEVICES REQUIRED TO PROTECT CONTAINMENT PENETRATION ASSEMBLY CONDUCTORS IN ACCORDANCE WITH THE REQUIREMENTS OF NRC REGULATORY GUIDE 1.43, REVISION 1, DATED MAY 1977, AS INTERPRETED BY 6/C JNC.
- 2. THIS DRAWING IS IDENTIFIED AS NUCLEAR SAFETY RELATED SINCE IT CONTAINS INFORMATION FOR BOTH NON-CLASS 1E AND CLASS 1E PROTECTION DEVICES.
- 3. ALTHOUGH NRC REGULATORY GUIDE 1.63 REQUIRES THE USE OF TWO PROTECTIVE DEVICES WHEN PENETRATIONS CANNOT WITHSTAND THE MAXIMUM FAULT CURRENT INDEFINITELY, IT PERMITS THE USE OF NON-CLASS 1E PROTECTIVE DEVICES FOR PROTECTION OF NON-CLASS 1E CIRCUITS.

TYPICAL PROCESS USED TO SIZE A PENETRATION PROTECTION DEVICE

A COMPARISON OF THE TIME VERSUS CURRENT WITHSTANDABILITY (I^2t) OF THE PENETRATION ASSEMBLY CONDUCTORS AND THE ASSOCIATED PROTECTIVE DEVICES SHALL BE PERFORMED TO ENSURE THAT BOTH PRIMARY AND BACKUP OVERCURRENT PROTECTIVE DEVICES ARE DESIGNED TO CLEAR A FAULT CONDITION PRIOR TO REACHING THE (I^2t) CAPABILITY OF THE ASSOCIATED PENETRATION ASSEMBLY CONDUCTORS.

- STEP 1. PLOT THE (I^2t) WITHSTANDABILITY OF THE PENETRATION ASSEMBLY CONDUCTOR ON A LOG-LOG TIME-CURRENT CURVE USING INFORMATION OBTAINED FROM TABLE 1.
- STEP 2. CALCULATE THE MAXIMUM AVAILABLE SHORT CIRCUIT CURRENT THAT COULD BE EXPERIENCED IF A BOLTED FAULT WERE TO OCCUR BETWEEN CONDUCTORS AT THE INNER END OF THE PENETRATION ASSEMBLY. NOTE THAT THIS STEP NEED NOT BE PERFORMED IF THE OVERCURRENT PROTECTIVE DEVICE TIME-CURRENT CHARACTERISTICS INDICATE A CLEARING TIME WHICH IS LESS THAN THE TIME REQUIRED TO REACH THE (I^2t) CURVE PLotted IN STEP 1 REGARDLESS OF THE SHORT CIRCUIT CURRENT VALUE (SEE NOTE 4).

TYPICAL METHODOLOGY USED TO ESTABLISH TEST SETPOINTS AND RESPONSE TIMES OF PENETRATION PROTECTION DEVICES

TEST SETPOINT AND ASSOCIATED RESPONSE TIME VALUES ARE ESTABLISHED TO ENSURE THAT THE PROTECTION DEVICES WILL OPERATE IN ACCORDANCE WITH MANUFACTURER'S DESIGN CHARACTERISTICS AND THAT THE OPERATION WILL OCCUR PRIOR TO REACHING THE (I^2t) CAPABILITY OF THEIR ASSOCIATED PENETRATION ASSEMBLY CONDUCTORS, WITH ADEQUATE MARGIN (I.E., THE POINT ESTABLISHED BY THE TEST SETPOINT/RESPONSE TIME VALUE SHOULD AS A MINIMUM BE EQUAL TO THAT ESTABLISHED BY THE MAXIMUM BAND OF THE PROTECTIVE DEVICE, AND SHOULD AS A MAXIMUM BE LESS THAN THE VALUE OF PENETRATION ASSEMBLY CONDUCTOR (I^2t) CAPABILITY ESTABLISHED BY CALCULATION AS PLOTTED ON A TIME VERSUS CURRENT CURVE).

480V SWITCHGEAR POWER CIRCUIT BREAKERS

THE METHOD USED TO ESTABLISH TEST SETPOINTS AND RESPONSE TIMES ASSOCIATED WITH 480V SWITCHGEAR POWER CIRCUIT BREAKERS IS AS FOLLOWS:

EXAMPLE: LONG-TIME DELAY = 3.1 LONG-TIME P.D. AMP SETTING SHORT-TIME DELAY = 1.5 X SHORT-TIME P.D. AMP SETTING INSTANTANEOUS DELAY = 1.5 X INSTANTANEOUS P.D. AMP SETTING

NOTE: SOLID STATE TRIP DEVICE TYPE 1721/3650 SELECTION, AND BIPOLAR SETTING INFORMATION SHOULD BE OBTAINED FROM 6/C, INC DRAWINGS 512-200-937.

FUSES

THE METHOD USED TO ESTABLISH TEST SETPOINTS AND RESPONSE TIMES OF FUSES IS AS FOLLOWS: ESTABLISH TEST SETPOINTS OF FUSES IN ACCORDANCE WITH VENDOR SUPPLIED RESISTANCE VALUES WHICH HAVE BEEN OBTAINED FOR EACH SPECIFIC TYPE AND SIZE OF FUSE USED. RESISTANCE VALUES AT 40°C WERE DERIVED BY CALCULATION USING 25°C VALUES PROVIDED BY THE FUSE MANUFACTURER.

TEST SET POINT RESISTANCE VALUES ARE AS FOLLOWS:

Table with columns: MANUFACTURER, TYPE AND SIZE, MATERIAL FUSE LINK, RESISTANCE IN MEGOHMS (MINIMUM AT 25°C (77°F), AT 40°C (104°F), MAXIMUM AT 25°C (77°F), MAXIMUM AT 40°C (104°F)). Rows include various fuse models and types like Buss, KTD, etc.

NOTES:

- 1. FUSES WHICH HAVE BEEN IN SERVICE SHALL BE ALLOWED TO COOL TO ROOM TEMPERATURE BEFORE RESISTANCE TEST MEASUREMENTS ARE MADE FOR CONSERVATION THE 40°C RESISTANCE VALUES ARE APPLICABLE FOR ESTABLISHING TEST SETPOINTS (EXCEPTIONS: BUSS 2432B5, PAK-10 FPM-R66, GOLD ALDRER, ADVON, A252B6, ADVON, AK30B9, AK26-1 DT-3, TR3-2R, TR3R, TR3RA, T800LITTEL FUSE 21400L, 21400S, 21400S FUSES SHALL BE TESTED AT 25°C (77°F) 15°F).
- 2. THE APPROPRIATE FUSE MANUFACTURER SHALL CONFIRM THAT THE MINIMUM RESISTANCE VALUES AT 25°C AND THE FUSE LINK MATERIAL, IF APPLICABLE FUSES INSTALLED COMPLIES WITH THE ABOVE INFORMATION.

NOTES:

- 1. THIS PROCESS IS NOT THE ONLY METHOD USED TO SIZE OVERCURRENT PROTECTIVE DEVICES.
- 2. THIS PROCESS DOES NOT ADDRESS OVERCURRENT PROTECTION OF LOAD EQUIPMENT OR ASSOCIATED FIELD CIRCUITS. IN MOST CASES THE ABOVE PROCEDURE IS USED TO DETERMINE WHETHER THE O/C PROTECTIVE DEVICES REQUIRED FOR LOAD EQUIPMENT AND FIELD CIRCUITS PROTECTION PROVIDE ADEQUATE PROTECTION FOR ASSOCIATED PENETRATION CONDUCTORS.

TABLE 1

THE FOLLOWING AMPERE VALUES WERE OBTAINED BY CALCULATION USING ICEA P-32-382 FORMULA AND D.G. O'BRIEN'S TEST REPORT NO. ER-262.

Table with columns: ITEM, CONDUCTOR SIZE, I^2t SECONDS, IF 1-8.1 SECONDS, IF 1-75 SECONDS, IF 1-100 SECONDS, IF 1-200 SECONDS, IF 1-300 SECONDS.

THE FOLLOWING AMPERE VALUES WERE OBTAINED FROM D.G. O'BRIEN REPORT NO. ER-262:

Table with columns: ITEM, CONTINUOUS CURRENT, 1800 SECOND CURRENT, 60 SECOND SHORT TIME OVERLOAD.

CONFIRMED BY D.G. O'BRIEN TESTS

NOTES:

- 1. THE 60 SECOND VALUE FOR #12 AVG CONDUCTORS IS LESS THAN 7X CONTINUOUS CURRENT RATING (FAILURES EXPERIENCED AT 140 AMPS).
- 2. 1800 MCM PENETRATION ASSEMBLY CONDUCTORS ARE USED SOLELY FOR 7.2 KV REACTOR COOLANT PUMPS, XPP-30A, B, AND C. THE (I^2t) WITHSTANDABILITY OF 1800 MCM PENETRATION ASSEMBLY CONDUCTORS WAS PLOTTED ON A LOG-LOG TIME-CURRENT CURVE USING INFORMATION SPECIFIED BY D.G. O'BRIEN SINCE ALL EXISTING 1800 MCM PENETRATION ASSEMBLY CONDUCTORS ARE USED (I.E., NO SPARES) AND SINCE FUTURE REQUIREMENTS FOR 1800 MCM PENETRATION ASSEMBLY CONDUCTORS ARE NOT ANTICIPATED, TIME-CURRENT VALUES ESTABLISHING (I^2t) WITHSTANDABILITY OF THE 1800 MCM CONDUCTORS WILL NOT BE IDENTIFIED.

THIS IS A NUCLEAR SAFETY RELATED DOCUMENT AND SHOULD BE HANDLED OR PERFORMED WITHOUT PROPER OCCURRENCE AND WRITER APPROVAL.

FSAR Figure 8G-1 SOUTH CAROLINA ELECTRIC & GAS COMPANY VIRKIL C. SIMMER NUCLEAR STATION ELECTRICAL CONTAINMENT PENETRATION CONDUCTOR OVERCURRENT PROTECTION DEVICES DESIGN ENGINEERING

Revision table with columns: NO., DATE, BY, REVISION.

