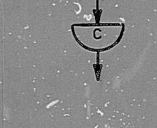


THIS DRA
:24 CMP




C-TDDO-TIME DELAY ON DROP OUT. WHEN AN INPUT IS PRESENT THE OUTPUT WILL BE PRESENT. WHEN THE INPUT IS REMOVED, THE OUTPUT WILL BE REMOVED AFTER A TIME DELAY.

SHARED QDPS AND PLANT
COMPUTER INPUT

SIGNAL IS HARDWIRED TO A QDPS CABINET AND
DATALINKED TO THE ERF DADS COMPUTER AND
SUBSEQUENTLY TO THE PLANT COMPUTER.

THIS DWG SUPERSEDES B&R DWG: 5Z-01-1-Z-2999-H
SH

TI
APERTURE
CARD

CHIEF ENG.		N/A																			 BECHTEL ENERGY CORPORATION HOUSTON, TEXAS										STANDARD LOGIC SYMBOLS			
																					HOUSTON LIGHTING & POWER CO. SOUTH TEXAS PROJECT										SCALE	JOB NO.	DRAWING NO.	REVISION
																															NONE	14826	5Z-01-9-Z-40418	1
			DWG NO	REFERENCE	DWG NO	REFERENCE	NO	DATE	REVISION	RE	CKR	CL	EGS	FE	NO	DATE	REVISION	RE	CKR	CL	EGS	FE												

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PDR RIDS

