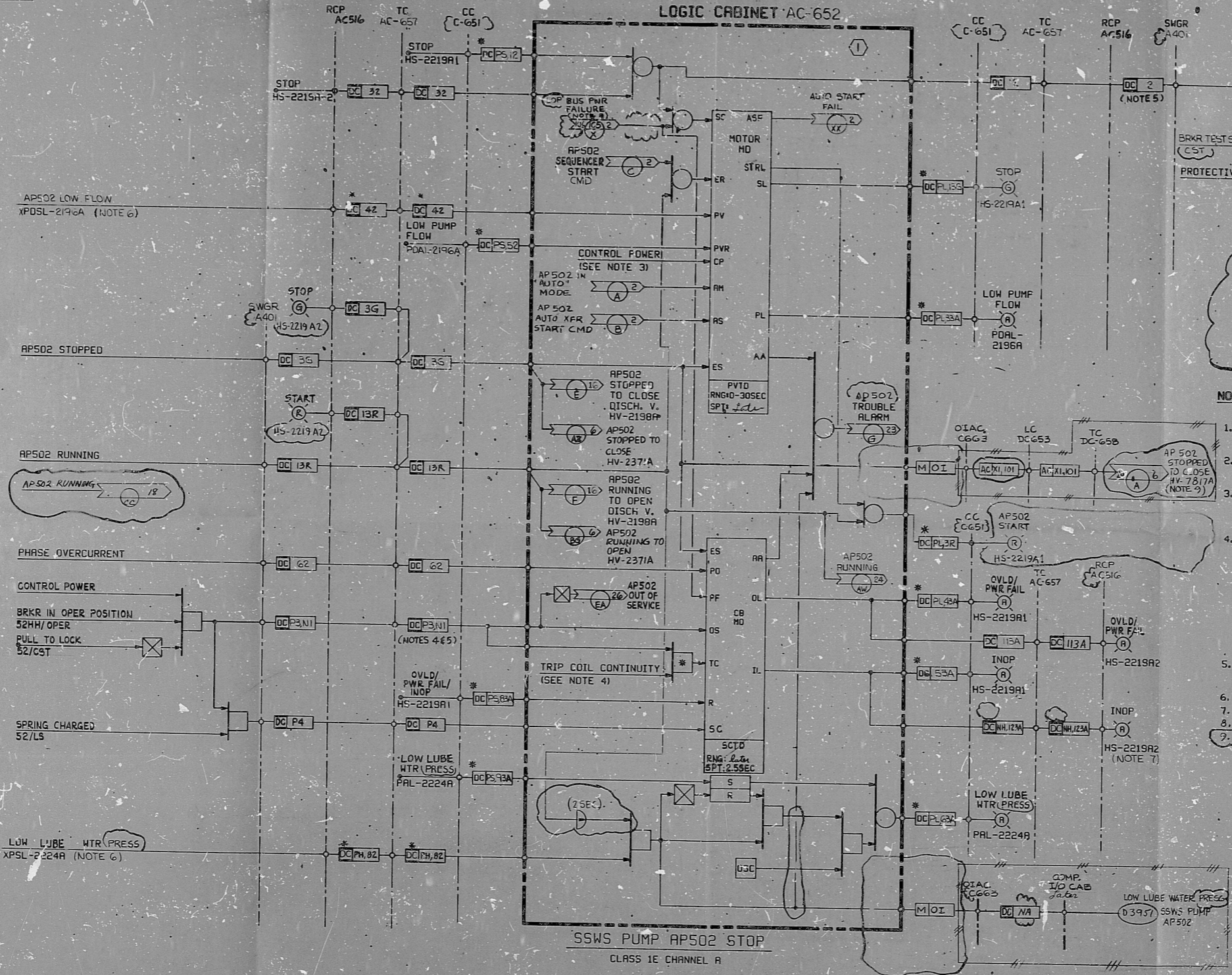


J-10-0 SHT. 3

LOGIC CABINET AC-652

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NOTES:

1. FOR START LOGIC AND CP502 CROSS REFERENCE SEE SHT. 2 OF THIS DRAWING.
2. FOR CONTROL SW LAYOUT SEE SHT. 2 OF THIS DRAWING.
3. A DUMMY CP INPUT GENERATED ON THE LOGIC CARD SHALL BE PROVIDED TO THE MOTOR MD TO PREVENT A FALSE ALARM.
4. TWO INPUTS ARE PROVIDED FOR WIRE NUMBER P3. THE RETURN OF ONE INPUT IS CONNECTED TO WIRE NUMBER N1 TO MONITOR CIRCUIT BREAKER OPERABILITY WHEN TRIPPED. THE RETURN OF THE OTHER INPUT IS CONNECTED TO WIRE NUMBER 2 TO MONITOR CIRCUIT BREAKER OPERABILITY AND TRIP COIL CONTINUITY WHEN CLOSED. THE "AND" GATE IDENTIFIED BY AN 'A' IS FORMED BY THE LATTER CONNECTION AND IS NOT PART OF THE SOLID STATE LOGIC.
5. POWER TO THE OUTPUT DRIVER CONTACTS FOR THE TRIP CIRCUIT IS SUPPLIED BY WIRE NUMBER P3.
6. CONTACTS OPEN ON CONDITION STATED.
7. CURRENT RATING 0.04 AMP.
8. RESETS FOLLOWING RESTORATION OF BUS POWER.
9. SEE PAID M24-0 SHEET 3, SERVICE WIR HYPOCHLORINATION.

NO.	DATE	REVISIONS	BY	CHKD	RECHKD	ENGR	PROJ	APPR
1	7/2/70	REVISED AS NOTED	JL	AL	AS	AK		
2	7/7/70	REVISED AS NOTED	JL	AL	AS	AK		
3	11/1/70	REVISED AS NOTED	JL	AL	AS	AK		
4	2/3/71	ISSUED FOR CONSTRUCTION	JL	AL	AS	AK		

BECHTEL
SAN FRANCISCO

PUBLIC SERVICE ELECTRIC AND GAS COMPANY
HOPE CREEK GENERATING STATION

LOGIC DIAGRAM
STATION SERVICE WATER SYSTEM
SSWS PUMPS AP502 & CP502 STOP

JOB No.	DRAWING No.	REV.
10355	J-10-0 SHT 3	6 Q

REV.	DATE	REVISIONS	BY	CHKD	PER.	APP.
1	7/2/70	REVISED BAILEY INTERFACE AND AS NOTED	JL	AL	AS	AK
2	7/7/70	DESIGN FREQUENCY ISSUE	JL	AL	AS	AK

S/U - EA

RIDS

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