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DOCKET No 50-219

August 19, 1988

Alexander W. Dromerick
 U.S. Nuclear Regulatory Commission
 1 White Flint North
 11555 Rockville Pike
 Rockville, Maryland 20852

Dear Mr. Dromerick:

Your letter dated November 10, 1987 concerning SEP Topic VII-1A, Isolation of Reactor Protection System for Non-Safety Systems, requested the following additional information:

- (1) circuit diagrams showing the isolation amplifier between the nuclear instrumentation analog signals and the multiplexer cabinet for the fuel management computer and
- (2) circuit diagrams and layout drawing which shows the electrical and spatial configuration.

The information requested was sent to you informally prior to our recent telephone conference call. During the phone conversation the staff concluded that the information supplied is sufficient and requested a re-submittal of the information.

Enclosed please find the following:

- Enclosure 1 Burr-Brown Isolation Amplifier Schematic
- Enclosure 2 RI05 IRM/APRM Process Recorder Internal Circuit Diagram
- Enclosure 3 Physical Description of RI05 IRM/APRM Process Recorder
- Enclosure 4 RPS Logic Schematic
- Enclosure 5 Nuclear Monitoring System Interface with RPS
- Enclosure 6 IRM/APRM General Arrangement
- Enclosure 7 Layout Drawing with RI-05 IRM/APRM Process Recorder Shown

We believe that the information being transmitted is sufficient to resolve the issues relating to the SEP Topic VII-1A.

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 P PNU

Very truly yours,

Yash K. Nagai
 Y. Nagai
 Senior Licensing Engineer

YN/pa(7236f)

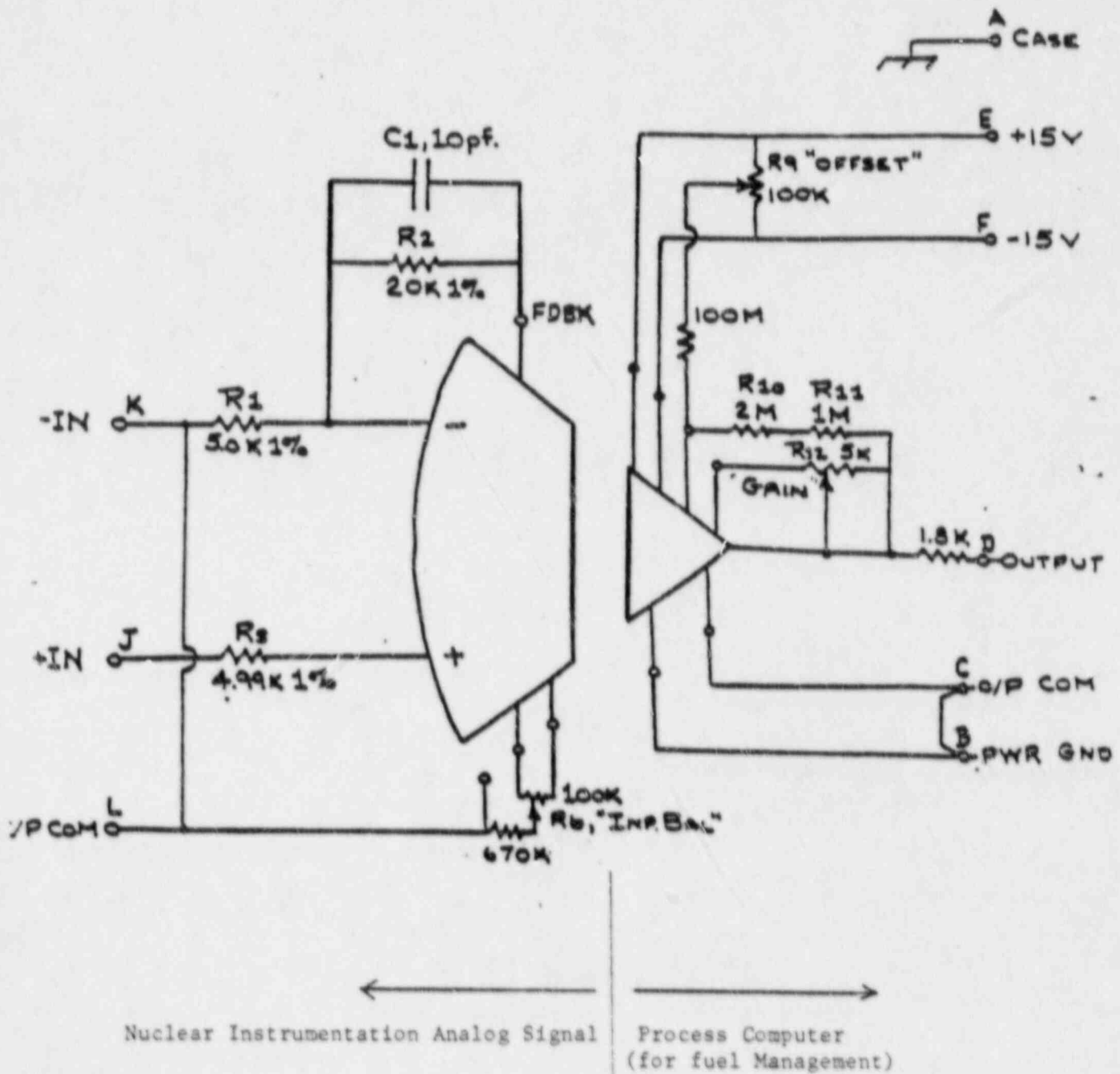
cc: M. W. Laggart w/o enclosure
 A. Agarwal
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BURR-BROWN ISOLATION AMPLIFIER
3451/16 SCHEMATIC

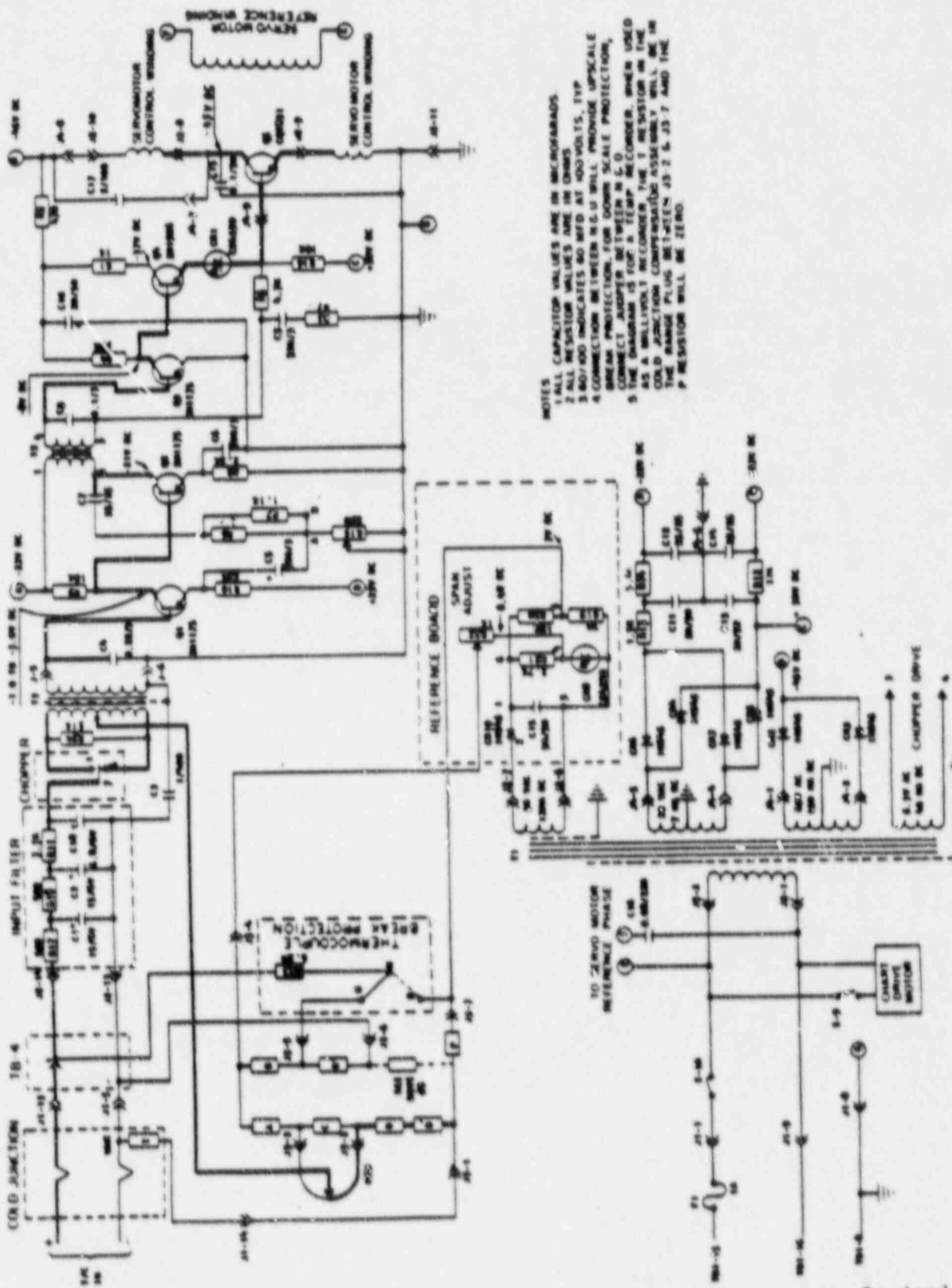
FIGURE 1003.1.6
Procedure 1003.1
Rev. 0 - 8/20/79



MILLIVOLT AND/OR TEMPERATURE MEASUREMENT

(Only one pen shown)

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NOTES

- 1 ALL CAPACITOR VALUES ARE IN MICROFARADS
- 2 ALL RESISTOR VALUES ARE IN OHMS
- 3 500/100 INDICATES 50 MFD AT 100 VOLTS, ETP
- 4 CONNECTION BETWEEN N.G.U WILL PROVIDE UPSCALE BREAK PROTECTION FOR DOWN SCALE PROTECTION,
- 5 THE DIAGRAM IS FOR A TEMP RECORDER, WHEN USED AS A MILLIVOLT RECORDER THE Y RESISTOR ON THE COLD COMPENSATION ASSEMBLY WILL BE IN THE RANGE PLUG BETWEEN J3-2 & J3-7 AND THE P RESISTOR WILL BE ZERO

Fig. 6. Schematic wiring diagram, potentiometric recorder

Revised Nov/63