NUCLEAR SAFETY RELATED INSTRUCTION MANUAL

FOR

ELECTRICAL PROTECTION

ASSEMBLY 914E175

PER

VPF ORIGINAL

GENERAL ELECTRIC COMPANY

NUCLEAR ENERGY DIVISION

SPECIFICATION 21A3120 MPL# C71S003 MPL# C72S003

FROM

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GENERAL

This manual is provided as the basic manual for Operation and Maintenance of the Electrical Protection Assembly (EPA) per General Electric Drawing Number 914E175.

The Electrical Protection Assembly (EPA) is composed of two basic subsystems:

- a) A General Electric Type TFJ-175A circuit breaker with companion TFK under-voltage release.
- b) Electrical protection logic printed circuit card.

The TFJ circuit breaker and electrical protection circuit card may be referred to as the Electrical Protection Assembly (EPA). This manual provides essential information for the operation and maintenance of the EPA unit, and detailed information for the operation and maintenance of the electrical protection logic circuit card.

SPECIFICATIONS

System Definition

The Electrical Protection Assembly (EPA) provides redundant protection to the Reactor Protection System (RPS), and other essential circuits against over-voltage, under-voltage and under-frequency. The EPA consists of trip components which disconnect circuitry from input power whenever voltage or frequency exceeds their normal tolerance.

Front Panel Control/Indicators

- 1. Indicators (Test/Maint.)
 - a) Over-voltage
 - b) Under-voltage
 - c) Under-frequency
 - d) Power IN
 - e) Power OUT
- 2. Indicators (Operation)
 - a) Input Power
 - b) Output Power
- 3. Controls
 - a) Main circuit breaker ON/OFF
 - b) Lockswitch for test maintenance use.

Electrical Requirements

Voltage: 120 Volts ±2%

Current Rating:

Start-up Current: 280MA for one second

Running Current: 250MA

Single Phase: Two Wire (plus ground)

Frequency: 50HZ/60HZ

Time Delay: Continuously Adjustable

0.1 to 3.0 Sec.

Circuit Breaker Max Load: 175 Amps A.C.

Operating Requirements

Temperature: 40° to 137°F

Humidity: Up to 95% Relative
Radiation: 2x10⁴ RAD, Silcon Total

Integrated Dose (TID) for

Group 1

2.0 X 10³ RAD, Silcon Total Integrated Dose (TID) for

Group 2

Altitude: 0-10,000 feet above sea level.

Mechanical

Weight: 60 lbs.

Height: 20"

Depth: 8"

Width: 16"

A hasp and staple are provided for padlocking the EPA enclosure. External feet are furnished for mounting.

Cables

Cable and conduit access openings to be provided by user at time of installation.

Seismic Qualification

- a) Operating base earthquake (OBE) 5.OG
- b) Safe Shutdown earthquake (SSE) 7.0G
- c) Frequency Spectrum 1 to 33 HZ

INSTALLATION

Mechanical Installation

The Electrical Protection Assembly (EPA) was designed for vertical wall mounting with external feet provided as an integral part of the enclosure, and provides protection from dirt, dust, oil and water. Preferred cable entrance and exit points are from top and bottom of assembly. The right side may be used as an alternate location if top and bottom prove to be impractical due to interference with proximity equipment.

Electrical Installation

Two EPA's are connected in series, on the output of each RPS motor-generator set.

Also, two EPA's are connected in series between each auxiliary power source which feeds auxiliary power to the RPS line.

OPERATION

General

Before attempting to operate the Electrical Protection Assembly (EPA), the operator should be thoroughly familiar with the operating instructions in this manual.

Front Panel Controls And Indicators Functional Description

1. Controls

A. Circuit Breaker

Controls output power to RPS bus and may be controlled manually or automatically by trip conditions.

8. Lockswitch

Provides administrative control of test mode when performing maintenance or calibration of trip points by tripping circuit breaker when lockswitch is in maintenance position. Lockswitch also transfers logic P.C. card transformer from circuit breaker power to front panel test jacks.

C. Test Jacks

Provides for calibration or test of trip parameters (over-voltage, under-voltage, and under-frequency).

2. Indicators

A. L.E.D. Data Lamps

Indicates a failure of the following motor generator parameters: Over-voltage, under-voltage, and under-frequency.

B. Incandescent Lamps

Indicates power into the EPA and power out of the EPA.

Logic Circuit Card Controls Functional Description

1. Controls

A. Over-Voltage Trip Point Setting R5

Sets the over-voltage detection trip point through the range of 96VRMS to 138VRMS.

B. Under-Voltage Trip Point Setting R6

Sets the under-voltage detection trip point through the range of 96VRMS to 138VRMS.

C. Under-Frequency Trip Point Setting R35

Sets the under-frequency detection trip point through the range 51Hz to 60Hz, and 42.5Hz to 50Hz.

D. Voltage to Frequency Gain Control R33

Sets frequency from voltage to frequency converter to 6000Hz (U3 out) for an input of 120 V nominal.

E. Time Delay Control R40, R54, R53

Continously adjusts time delay through the range of 0.1 to 3.0 seconds. An independent time delay is provided for each of the over-voltage, under-voltage, and under-frequency functions.

- 2. Indicators
 - A. No indicators are provided on the logic card. See front panel indicators during calibration or test.

Preliminary To Operation

Prior to any Electrical Protection Assembly (EPA) operation, the installation procedure should have been accomplished and the operator should be knowledgeable as to the function of the EPA controls and indicators.

Initial Power Turn On

The following initial power up sequence should be followed:

- Facing the EPA enclosure with the door open to the left, verify that the power line cables are connected to the circuit breaker as follows: The neutral conductor should be on the right terminal (top and bottom), hot conductor should be on left terminal (top and bottom), and ground conductor should be on the terminal lugs provided on inside panel.
- 2. Close the door. Verify lockswitch is in the normal position (CCW).
- With power applied, verify that "power in" indicator is lighted, and observe that over-voltage, under-voltage, and under-frequency indicators, and "power out" indicator are out.
- 4. Activate EPA circuit breaker to the ON position, and verify that "power out" indicator is lighted.
- The EPA is now on-line and operational.
 - NOTE: If any of the three failure indicators are illuminated during the power-up sequence, the circuit breaker will not set, even if circuit breaker is held in "ON" position.

Power Turn Off

Power to the RPS bus can be removed by throwing the circuit breaker to "OFF". Power will remain on EPA logic circuit card as long as the "Power in" indicator remains lighted.

THEORY OF OPERATION

Introduction

This section deals with the overall theory of operation of the 914E175 Electrical Protection Assembly (EPA). General Electric Drawing 184C4476 is contained in this manual and provides a schematic for the EPA system.

General System Operation

- A. The 914E175 Electrical Protection Assembly (EPA) is intended to provide protection for the Reactor Protection System (RPS) of a boiling water reactor power plant. The EPA provides redundant protection against power source transitory over-voltage, under-voltage, and under-frequency conditions. Two EPA's are sonnected between each of the two RPS motor-generator sets and the RPS buses; also two series-wired EPA's are connected between the auxiliary power source and the RPS buses.
- B. The EPA's over-voltage and under-voltage trip points are adjustable over the range of 96 VRMS to 138 VRMS of the nominal 120 VRMS A-C for a sine wave of 40 to 70 Hz. The EPA under-frequency trip point is adjustable from 42.5 Hz to 60 Hz. Whenever the A-C voltage deviates from the nominal value by +10% or -10%, or the frequency decreases below the nominal value of 60Hz -5% or 50Hz -5%, the EPA shall disconnect the power to the RPS bus.
- C. The EPA incorporates three independent continuously adjustable time delay trip mechanisms with a range from 0.1 second to 3.0 seconds, and are factory set at 0.1 second +0.03 seconds.
- D. Prior to colibration/maintenance, control is exercised through the lockswitch on the EPA to disconnect the electrical source from the RPS bus.

Detection And Logic Card Operation

A. Circuit card power is provided from a power transformer with the primary across the upstream side of the circuit breaker. A trip condition of the circuit breaker will not disconnect the circuit card from line power.

B. Under-Frequency Detection

Line parameters are monitored via one transformer T2 and a resistive voltage divider on TB1 which provides 7.5 volts RMS nominal to the logic card.

The 7.5 volts is fed to both the precision full wave A-C to D-C rectifier (U1), and a signal conditioning circuit (U11). The conditioned 60 Hz nominal square wave is a reference for the Phase Lock Loop Multiplier (U12, 13 and 14) which outputs 6000 Hz for a 60 Hz nominal input. This 6000 Hz is used as a clock for the precision under-frequency detector (U15, U16A), which has an adjustable range (R35) from 5100 Hz to 6000 Hz and 4250 Hz to 5000 Hz. Under-frequency detection is accomplished by comparing alternately charging and discharging RC networks, with the discharge rate of the fixed reference network (C16, R49). When the frequency falls below the detection point, the output of the high speed comparator (U16A) is a pulse train whose repetition rate equals that of the clock input and whose width is proportional to the difference between the actual frequency and the frequency set of the detector.

As the frequency get lower, the pulse width increases. The pulse train is converted to a constant high level, using a pre-triggerable one-shot multi-vibrator (U17A) whose period is approximately 20% longer than the period of the lowest frequency. When the output of the one-shot goes high, it commands the time (U18) to start through its time sequence. The timer delay is variable from 0.1 to 3 seconds with R53. After the delay is finished, the output up-clock is converted to a high level with the dual D Flip Flop (U19A). The high level goes to a three input nor-gate (U20) which turns off Q4 and in turn removes the ground from one side of the under-voltage coil in the circuit breaker. With the removal of current through the under-voltage coil, the circuit breaker trips removing power from the RPS bus.

NOTE: If the line frequency returns to nominal before the timer sequence runs its full period, the detection and timer circuitry will reset to normal without tripping the circuit breaker.

C. Under-Voltage Detection

The A-C to D-C converter (U1) is a precision circuit that converts the nominal A-C voltage to a nominal D-C voltage which controls a precision voltage to frequency converter (U2) with a nominal output of 12000 Hz. The 12000 Hz branches at this point to the over-voltage circuit and undervoltage circuit. The over-voltage description will be discussed in the next section. The 12000 Hz is converted to 6000 Hz with a 50% duty cycle in the dual D Flip Flop (U3B) which generates the clock for the under-frequency detector (U6 and U5B).

As the nominal voltage decreases toward the detection trip point, it is precisely tracked by the A-C to D-C converter, and the voltage to frequency converter which proportionately lowers the 6000 Hz from its nominal value. Under-frequency detection is then accomplished by comparing alternately charging and discharging RC networks with the discharge rate of the fixed reference network (C23) (R23).

When the frequency falls below the detection point, the output of the high speed comparator (U5B) is a pulse train whose repetition rate equals that of the clock input and whose width is proportional to the difference between the actual frequency and the frequency set of the detector. As the frequency gets lower the pulse width increases. The pulse train from the detector circuit is converted to a constant high level, using a pretriggerable one-shot (U7B) whose period is selected to be approximately 20% longer than the period of the lowest frequency.

When the output of the one-shot multi-vibrator goes high, it commands the timer (U10) to start through its time sequence. This sequence is variable from 0.1 to 3 seconds with R54, and when the time sequence is finished, the output up-clock only is converied to a high level with the dual D Flip Flop (U8B). The high level is then gated through a three input nor-gate which turns off Q4 and in turn removes the ground from one side of the under-voltage coil tripping the circuit breaker.

NOTE: If the line voltage returns to nominal before the timer sequence runs its full period, the detection and timer circuitry will reset to normal without tripping the circuit breaker.

D. Over-Voltage Detection

Like the under-voltage circuit, the 12000 Hz is converted to 6000 Hz @ 50% duty cycle in a dual D Flip Flop (U3A) which generates the clock for the over-frequency detector. As the nominal voltage increases toward the detection trip point, it is precisely tracked by the A-C to D-C convertor which proportionately raises the 6000 Hz from its nominal value. Over-frequency detection is then accomplished by comparing alternately charging and discharging RC networks, with the discharge rate of the reference network (C22, R21) fixed. When the frequency rises above the detection point, the pulse train (whose repetition rate equals that of the clock input, and whose width is proportional to the difference between the actual frequency and frequency set of the detector) is removed from the pretriggerable one-shot (U7A) which makes the output return to a low level. The output goes to a high level at this time and commands the timer to start through its time sequence. This time sequence is variable from 0.1 to 3 seconds with R40.

When the time sequence is finished the output up-clock only is converted to a high level with the dual D Flip Flop (U8A). This high level is then gated through a three input nor-gate which turns off Q4 and in turn removes the ground from one side of the under-voltage coil in the circuit breaker. With the removal of current from the under-voltage coil, the circuit breaker trips removing power from the RPS bus.

NOTE: If the line voltage returns to nominal before the timer sequence runs its full period, the detection and timer circuitry will reset to normal without tripping the circuit breaker.

CALIBRATION AND ADJUSTMENT

General

A. The 914E175 Electrical Protection Assembly (EPA) has seven adjustable controls located on the printed circuit cará assembly. One controls the gain of the voltage to frequency converter. Three adjust the trip points of the over-voltage, under-voltage, and under-frequency detectors. Three independently adjust the time delay of the over-voltage, under-voltage, and under-frequency functions.

NOTE: Before performing any test or adjustment on an EPA, verify that it is not in service.

B. The following is a list of minimum requirements for the motor generator simulator, used to calibrate and maintain the Electrical Protection Assembly.

Frequency Range	35 Hz to 100 Hz in a single decade range
Resolution	0.1 Hz for the 35 Hz to 100 Hz range
Frequency Stability	10PPM/°C- 20 PPM/year
Waveform	Sine wave + 5%, -5% distortion
Amplitude	Continuously adjustable from 0 to 150 Volts RMS (0 to 425 volts peak to peak)
Amplitude Stability	

- C. The nominal line voltage is defined here as the value of voltage measured at the input of the EPA while delivering sufficient current to the scram discharge coils to drop 115 volts RMS across the coils. The upper trip point is set at minus 10% below nominal line voltage.
- D. Preventive maintenance checks shall be performed every one hundred eighty (180) days and consist of the following:
 - 1. Verification of trip point settings (recalibrate if required).
 - 2. Visual examination for dust, oil, and moisture (clean and dry internal components).
 - Visual examination of critical joint areas of circuit breaker terminals, and jumper bar for signs
 of corrosion (clean corrosion and apply liberal amounts of Penetrox "A" compound).

Adjustment Procedure

Voltage To Frequency Converter Gain Adjustment

With the Electrical Protection Assembly powered up, verify with a digital A-C voltmeter that there is 7.5 VRMS at ST1. (It is assumed that 120 VRMS exists at the input to the primary of T1, and that power from the power source is present.) Connect digital frequency meter at Pin 3 of U2, and adjust R33 until the frequency counter reads 1200 Hz(+) or (-)60 Hz. Move digital frequency meter to U3A and verify that 6000 Hz clock is present at Pins 1, 2, 12 and 13 of U3.

Over-Voltage Trip Point Adjustment

Connect an Oscilloscope or suitable logic level indicator to U7 Pin 7, and verify a low state exists when the line voltage is nominal. With R5 adjusted clockwise, raise the line voltage to the upper trip point, and slowly turn R5 counter-clockwise until the indicator at Pin 7 just changes state without blinking, or a solid unbroken line appears on Oscilloscope. This change of state indicates a trip condition, and is also the command to start the timer sequence. With R5 set at the trip point, readjust the line voltage back to nominal line voltage and verify U7A Pin 7 returns to a low state. Without changing R5, slowly raise the line voltage towards the trip point. The level at U7A Pin 7 should change states between the limits of the trip point and 2.5% below the trip point. Repeat this last procedure five (5) times to verify repeatability of trip point setting. Return the line voltage to nominal.

Over-Voltage Time Delay Adjustment

Connect an Oscilloscope or suitable logic level indicator to ST5 and verify a low state exists when the line voltage is nominal. With R40 adjusted fully clockwise, slowly raise the line voltage towards the upper trip point. When the trip point is reached, verify by observing U7A Pin 7. The time from trip to a change in state at ST5 to a high level should not be less than 0.1 second nor more than 0.133 seconds. Return the line voltage to nominal, and adjust R40 fully counter-clockwise, and slowly raise the line voltage toward the upper trip point. When the trip point is reached, the time from trip to a change in state at ST5 should not be less than 3 seconds nor more than 4 seconds. Return the line voltage to nominal, and readjust R40 fully clockwise.

NOTE: This sets in minimum delay.

Under-Voltage Trip Point Adjustment

Connect an Oscilloscope or suitable logic level indicator to U7B Pin 10, and verify a low state is present when the line voltage is nominal. With R6 adjusted clockwise, lower the line voltage to the lower trip point, and slowly turn R6 counter-clockwise until the indicator at Pin 10 changes state without blinking or a solid unbroken line appears on Oscilloscope. This change of state indicates a trip condition, and is also the command to start the timer sequence. With R6 set at the trip point, readjust the line voltage back to nominal and verify U7B Pin 10 returns to a low state. Without changing R6, slowly lower the line voltage towards the lower trip point. The level at U7B Pin 10 should change states between the limits. Repeat this last procedure five (5) times to verify repeatability of trip point setting. Return the line voltage to nominal.

Under-Voltage Time Delay Adjustment

Connect an Oscilloscope and/or suitable logic level indicator to ST6 and verify a low state exists when the line voltage is nominal. With R54 adjusted fully clockwise, slowly lower the line voltage toward nominal. When the trip point is reached (verify by observing U7B Pin 10 per Section 5) the time from trip to a change in state at ST6 to a high level should not be less than 0.1 seconds or more than 0.133 seconds. Return the line voltage to nominal, and adjust R54 fully counter clockwise. Slowly lower the line voltage toward the lower trip point. When the trip point is reached, the time from trip to a change in state at ST6 should not be less than 3 seconds nor more than 4 seconds. Return the line voltage to nominal, and readjust R54 fully clockwise.

NOTE: This sets minimum delay.)

Under-Frequency Trip Point Adjustment (60HZ)

Connect an Oscilloscope to U12 Pin 4, and verify that the Phase Lock Loop is outputing 6000Hz ± 10Hz. Move Oscilloscope and/or frequency counter to U11 Pin 10 and verify that 6000Hz clock is present at the under-frequency detector. Move the Oscilloscope and/or a suitable logic level indicator to U17A Pin 6. Verify a low state exists when the line frequency is 60Hz nominal, and R35 is adjusted fully counter clockwise. Using the digital selector switches on the power source simulator, adjust the line frequency to 57 60Hz. Slowly adjust R35 clockwise until the indicator at Pin 6 changes state without blinking or a solid unbroken line appears on the Oscilloscope. This change of state indicates a trip condition, and also starts the timer sequence. Without changing R35 return the line frequency to 60Hz. Lower the line frequency toward 57.00Hz. Repeat this last procedure five times to verify repeatability of the trip point setting. Return line frequency to 60Hz when finished.

Under-Frequency Trip Point Adjustment (50HZ)

The procedure will be the same as in the above section, except the motor generator simulator will use 50Hz as a nominal frequency. When adjusting R35 the trip point setting will be 47.8Hz with a trip range of 47.5Hz minimum to 48.45Hz maximum.

Under-Frequency Time Delay Adjustment

Connect an Oscilloscope and/or suitable logic level indicator to ST7 and verify a low state exists when the line frequency is 60Hz nominal or 50Hz nominal as in the above two sections. With R53 fully clockwise, and using digital switches on simulator adjust the frequency toward 57.00Hz. With R53 fully clockwise, and using digital switches on simulator adjust the frequency toward 57.00Hz or 47.5Hz (60Hz or 50Hz sections above). When the trip point is reached (verify by observing U17A Pin 6 as per above two sections) the time from trip to a change in state at ST7 to a high level should not be less than 0.1 seconds nor more than 0.133 seconds. Return the line frequency to 60Hz/50Hz nominal, and adjust R54 fully counter clock-wise. Lower the frequency toward 57.00Hz/47.5Hz. When the trip point is reached the time from trip to a change in state at ST7 should not be less than 3 seconds nor more than 4 seconds. Return the line frequency to 60Hz/50Hz nominal, and readjust R53 clockwise.

NOTE: This sets minimum delay.

This concludes the calibration and adjustment of the Electrical Protection Assembly (EPA).

TROUBLESHOOTING

Philosophy

Troubleshooting should only be preformed by qualified technical personnel. The troubleshooting portion of this manual is provided to assist a troubleshooter in pinpointing a specific problem to a specific section of the assembly. If the assembly is on line and operational, the troubleshooter will have to first take the EPA out of service, then insert the assembly key into the lockswitch and turn it clockwise to "Cal/Maintenance" position. This will remove power from the EPA output, and place the logic card input across the two test jacks on the front panel.

A simulated signal may be inserted at the test jacks for troubleshooting the EPA.

CAUTION: Don't bet your life on the "power in" incandescent light bulb being good. After opening the front door, verify that zero voltage is present on the 120 volt input to the circuit breaker. There is no interlock switch in the EPA.

Initial Troubleshooting

Place a proper signal in the input test jacks with the lockswitch in the "Cal/Maintenance" position. If the over-voltage, under-voltage and under-frequency light emitting diodes (LED'S) are off, reset the circuit breaker. If the circuit breaker fails to reset, check the voltage at terminals 4 and 5 of TB1.

If approximately 14 volts D-C is present, then the circuit breaker is defective. If no voltage is present, then check for A-C voltage across ST2 and ST4 on the printed circuit board. If no A-C voltage is present, possibly transformer T1 is defective.

If the over-voltage or under-voltage LED is on, check for approximately 8 volts RMS at ST1 and ST3 on the printed circuit card. If incorrect, check resistors RS1 and RS2. If the resistors are all right, then check transformer T2 for proper operation.

If the under-frequency LED is on when the correct frequency is at terminal ST1 on the P.C. Card, the P.C. Card is defective and must be replaced.

NOTE: Field Service maintenance is at the printed circuit card level only. For component level maintenance, the faulty printed circuit card should be returned to GE Field Service Operation for repair, and return to spares.

Setpoint Verification

1. Transfer applicable RPS to a different power source.

NOTE: Since an RPS bus can be connected to only one power source at a time, this will cause a brief half scram condition.

2. Put "keylock" switch in "Test" position. (Circuit breaker will trip.)

NOTE: Opening of the EPA Panel door is not required for setpoint verification.

- Insert test source signal in Test Jacks and adjust to required voltage to simulate proper voltage at scram coils. Reset the circuit breaker.
- 4. Increase voltage until over-voltage logic and circuit breaker trips. Verify that logic trips within technical specification limits. Reset circuit breaker. If trip point is not within limits, adjust the setpoint according to the procedure on page 9 of this manual.
- 5. Repeat procedure for under-voltage and under-frequency.
- 6. Remove test source, return "keylock" switch to "Normal" position and reset circuit breaker.
- 7. Transfer RPS bus back to original power source.

DOMESTIC PACKING AND SHIPPING PROCEDURE

1.0 Scope

This document defines the specific requirements for the domestic packing and shipping of "Electrical Protection Assemblies".

2.0 Cleaning

Each item to be packaged shall be carefully examined, any dirt, oil, grease, or other foreign material shall be removed prior to packaging.

3.0 Packaging

The materials to be shipped shall be packaged four (4) or six (6) to a container, and shall contain the following items:

4/6 - EPA Enclosures

4/6 - Pairs of lockswitch keys (one pair per enclosure).

Each pair of lockswitch keys shall be packaged in a stock control envelope with complete part identification clearly marked on the outer surface. The envelope shall then be taped on the inside of the enclosure door.

4.0 Vapor Barrier

- 4.1 The enclosure shall be placed in a bag-like container made from MIL-B-131 material. Also, the container will include in this bag, a one-pound bag of desiccant (MIL-D-3464, Type II). Install the humidity indicator in skin of vapor barrier bag with the indicator side out to provide visibility for inspection for desiccant saturation.
- 4.2 The vapor barrier bag shall then be evacuated using a suitable vacuum pump and sealed using a leak-proof heat sealing method.
- 4.3 Place the sealed leak-proof baglike container in the box constructed per paragraph 5.0 making sure the bag is sufficiently blocked, braced, and secured within the container so as to prevent shifting during transit. Also, make sure the bag is adequately cushioned in a manner that will protect its integrity during transportation and handling.
- 4.4 Place one copy of the packing list sealed in a vapor barrier material envelope. This envelope shall be clearly marked "Packing List" with a minimum 1/2" letter size.
- 4.5 Seal the box using procedures described in paragraph 5.3. Securely attach one copy of the packing list, sealed in a waterproof envelope, to the outside surface of the shipping container.

5.0 Box Construction

- 5.1 The shipping container shall be built to the dimensions shown in Shipping Container Drawing #147D7802 as applicable.
- 5.2 The box shall be constructed as shown in Drawing # 147D7802. All lumber shall be sound, well seasoned, commercial grade. Any knots shall be sound and not in excess of 1/3 the width of any board. Nails shall be driven into cleats in a staggered pattern. When assembling the panels, nails shall be driven in a straight pattern.

5.3 All nails shall be cement-coated or chemically etched cooler, sinks or standard box nails. Nails shall be sized so that there will not be less than 33 percent penetration of the secondary box member. For maximum strength, they shall be driven into the side grain of the lumber used for packaging. End grain nailing shall not be permitted. Lag screws or bolts and nuts may be used, instead of nails.

6.0 Marking

All marking described	below shall	appear	on one	side	and	the	top	of	each	container.
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1.	Control Number: Shipping notice number.
2.	Case Number: *X/
	*X denotes box number
3.	MPL#:
4.	Part Number: Rev
5.	Part Name: Electrical Protection Assembly
6.	Legend: "Made in U.S.A."
7.	From: General Electric Co.
	Nuclear Energy System Division San Jose, CA U.S.A.
8.	Dimensions: in
	Length
	Width
	Height
9.	Cube: cu. ft
10.	LB.
	Gross Weight
	Legal Weight
	Net Weight
11.	Handling Marks
	Fragile: (Wine glass)
	Sling Here: (Segments of chain links)
	No Hooks: (Grappling hook with cross)
	Up: (Two vertical arrows with underscore)
	Keep Dry: (Umbrella with rain drops)
	Center of Gravity: (Triangle with horizontal bar centered at apex.)
12.	Desiccant: Desic (No. of bags installed)
13.	Date:
14.	Procedure Number:
15.	Do not stack over 4 high.

EXPORT PACKING AND SHIPPING PROCEDURE

1.0 Scope

This document defines the specific requirements for the export packing and shipping of "Electrical Protection Assemblies".

2.0 Cleaning

Each item to be packaged shall be carefully examined and any dirt, oil, grease, or other foreign material shall be removed prior to packaging.

3.0 Packaging

The materials to be shipped shall be packaged four or six to a container, determined by shipping container group number used and shall contain the following items:

4 or 6 - E.P.A. Enclosures

4 or 6 - Sets of lockswitch keys

Lockswitch keys shall each be packaged in a stock control envelope with complete part identification clearly marked on the outer surface. The package shall then be taped to the inside of the enclosure door.

4.0 Vapor Barrier

- 4.1 The enclosure shall be placed in a bag-like container made from Mil-B-131 material. Also, include in this bag, a one pound bag of desiccant Mil-D-3464, Type II, non-dusting type. Install the humidity indicator in skin of vapor barrier bag with the indicator side out to provide visibility for inspection of desiccant saturation.
- 4.2 The vapor barrier bag shall then be evacuated using a suitable vacuum pump and sealed using a leak-proof heat sealing method.
- 4.3 Place the sealed leak-proof bag in the box constructed per paragraph 5.0 making sure the bag is sufficently blocked, braced, and secured within the container so as to prevent shifting during transit. Also, make sure the bag is adequately cushioned in a manner that will protect its integrity during transportation and handling.

- 4.4 Place one copy of the packing list inside the container sealed in a vapor barrier material envelope. This envelope shall be clearly marked "Packing List" with a minimum 1/2" letter size.
- 4.5 Seal the box using procedures described in paragraph 5.0. Securely attach one copy of the packing list, sealed in a waterproof envelope, to the outside surface of this shipping container.
- 5.0 Box Construction
- 5.1 The shipping container shall be built to the dimensions shown in DWG #147D7802.
- 5.2 The box shall be constructed as shown in DWG #147D7802. All lumber shall be sound, well seasoned, commercial grade. Any knots shall be sound and not in excess of 1/3 the width of any board. Nails shall be driven into cleats in a staggered pattern. When assembling the panels, nails shall be driven in a straight pattern.
- 5.3 All nails shall be cement-coated or chemically etched cooler, sinks, or standard box nails. Nails shall be sized so that there will be not less than 33 percent penetration of the secondary box member. For maximum strength, they shall be driven into the side grain of the lumber used for packaging. End grain nailing shall not be permitted. Lag screws or bolts and nuts may be used, instead of nails.
- 5.4 Each panel of shipping container must be lined with a laminated asphaltic waterproof kraft paper and all kraft joints shall be waterproof glued when assembling. (use PPP-B-1055, Class L-2b or Buyer approved equivalent.)
- Cushioning will be provided by strapping and bolting (use rubber washer and contact cement under washer for waterproofing) the wooden shipping container to a "floating deck" type skid base. Follow details outlined below for construction of the skid base. See Figure 1 for example.

 NOTE: Strap shipping container to "floating deck" only not to skid base.
- 6.1.1 Fabricate a deck whose length and width is dimensionally the same as the base of the shipping container. Use 3/4 inch CD-X plywood for material.
- 6.1.2 Fabricate a skid base whose length and width is 2 inches larger than the floating deck described in step "1" above.
- 6.1.3 Cut strips of shock cushioning material. Material may be 2 inch thick ethafoam, 2 lb./ft. or 2" thick polyester polyurethane foam, 4 lb./ft. Quantity of cushioning material is dependent upon the weight of the item being shipped. If using ethafoam, the static loading should be approximately 2 lbs./in. (i.e., unit weighs 400 lbs., then 200 total square inches of foam required). If using polyester foam, the static loading should be approximately 4 lbs./in. (i.e., unit weighs 400 lbs., then 1,000 total square inches of foam required.)

side and the top of each container.

- 6.1.4 The assembly sequence should begin first by locating and nailing 1/4 in. (min.) plywood "land strips" to waterproof skid base. These plywood strips will be dimensionally the same size as the foam strips. Spacing of these plywood strips to be equal distant with the end two strips ALWAYS on the edge of the deck.
- 6.1.5 Bond respective foam strips to plywood strips using contact adhesive (Scotch Insulation Adhesive #34 or "Weldwood Contact Cement, U.S. Plywood Corp.) or Engineering approved equivalent. Follow precautionary instructions printed on label, specifying product usage carefully. When using contact cements, it is important to roll or brush on adhesive on both contacting surfaces; allow a 10-15 minute drying time or until tacky, then bond together.
- Bond plywood deck to foam strips in the same manner as described in step "5" above. 6.1.6
- 6.1.7 The four 1/2" carriage bolts in each corner of the deck will be tightened until foam cushioning material just starts to compress then locked with the double nuts. This completes the "floating deck" assembly.

7.0	Mar	king				
	All	marking descr	ibed be	low s	hall appear on or	ne
	1.	Control Numi	ber: Ship	pping	notice number.	
	2.	Case Numbe	r:		*X/	
		* X	denotes	box	number	
	3.	MPL#:				
	4.	Part Number			Rev	
	5.	Part Name: E	lectrical	Prote	ection Assembly	
	6.	Legend: "Ma	de in U.	S.A."		
	7.	From: Genera	al Electri	ic Co.		
		Nuclea	r Energy	Syste	em Division	
7.0		San Jos	se, CA L	J.S.A.		
	8.	Dimensions:		in.	cm.	
			Length			
			Width	-		
			Height			
	9.	Cube: cu. ft.		cu. n	1	
	10.		LB.		G.	
		Gross Weight				

Legal Weight Net Weight

Markins

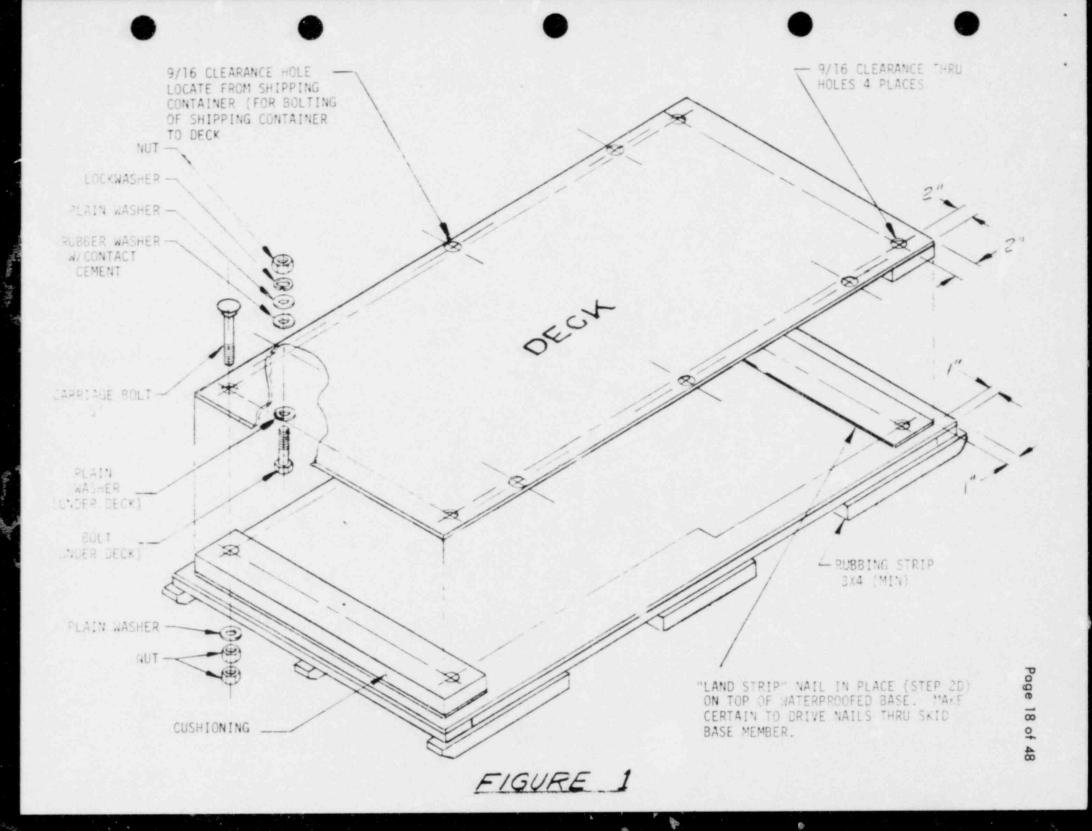
11. Handling Marks

Fragile: (Wine glass)

Sling Here: (Segments of chain links)
No Hooks: (Grappling hook with cross)
Up: (Two vertical arrows with underscore)
Keep Dry: (Umbrella with rain drops)

Center of Gravity: (Triangle with horizontal bar centered at apex.)

- 12. Desiccant: Desic _____(No. of bags installed)
- 13. Date:
- 14. Procedure Number:
- 15. Do not stack



STORAGE INSTRUCTIONS

1.0 Scope

- 1.1 This document defines the specific requirements for inspection and preventive maintenance for the Electrical Protection Assembly during storage. The product's integrity shall be protected with provisions for both short term (0-1 year) and long term (1 to 5 year) storage.
- 1.2 The G.E. supplied storage instructions will be used to convey to the storage agencies the required protective measures to ensure stored equipment is suitable for eventual installation in the reactor complex.

2.0 Applicable Documents

21A8754 Seller (Vendor) Supplied Storage instructions.

3.0 Storage Environments

- 3.1 The Electrical Protection Assembly Shipping container shall be stored off the floor on suitable skids, pallets, or racks in a permanent or "Butler" type building which affords environmental protection. Each E.P.A. enclosure is sealed (at time of shipping) in a vapor bar. ier bag, together with a bag of Type II silica gel desiccant as controlled protection against internal condensation.
- 3.2 Further protection against internal moisture is provided with humidity indicators installed in the skin of the vapor barrier bag.

4.0 Inspection of Humidity Indicators

- 4.1 The humidity indicators shall be inspected once every six months to determine if there is any danger of the desiccant being saturated (change desiccant if 30% RH dot turns pink).
- 4.2 When reading the indicator, note that when the humidity is below the stated calibration of the particular spot that spot will be a distinct blue. When the humidity is higher than this calibration, the spot will be a distinct pink. The bluer the color, the drier is the air, and vice versa.
- 4.3 The indicators, if exposed to extreme heat, prolonged exposure to the sun, and normal outside conditions for six months or over, will turn from a blue indication to a greenish-blue. This is caused by the yellowing of the blotter paper combining with the blue color of the chemical. This however, does not affect its calibration.

5.0 Preventive Maintenance Procedure

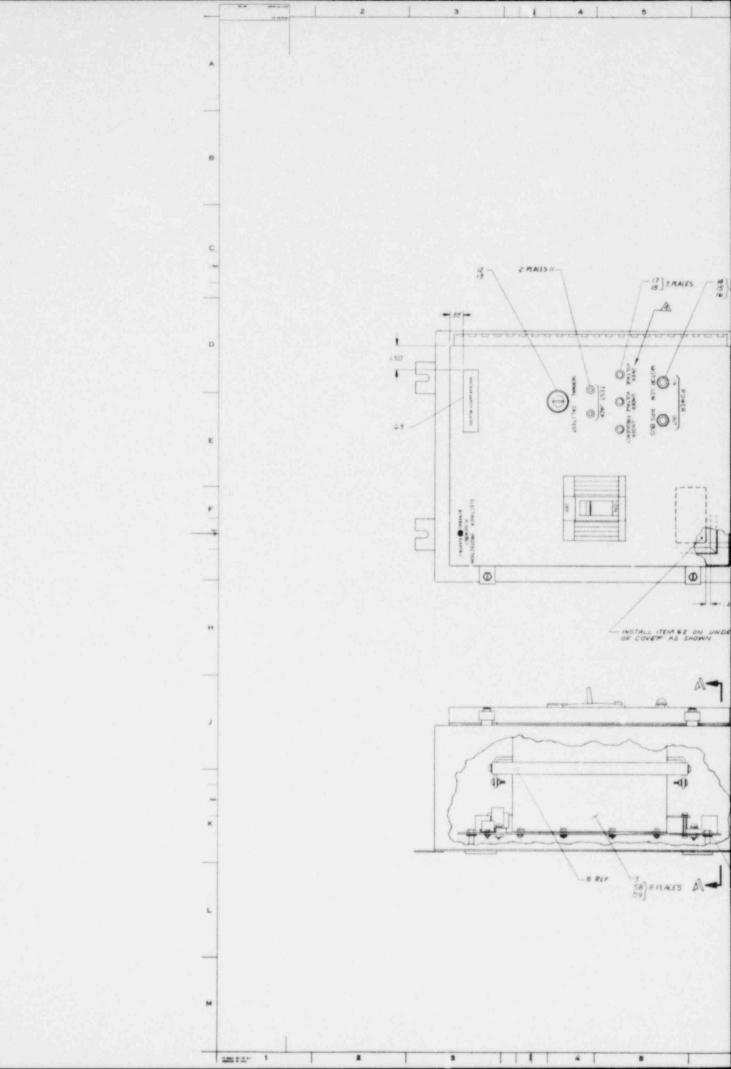
If a dangerous moisture level is determined to be present by a reading on the humidity indicator, the following procedure should be followed by storage facility personnel to reestablish the low moisture environment inside the vapor barrier bag.

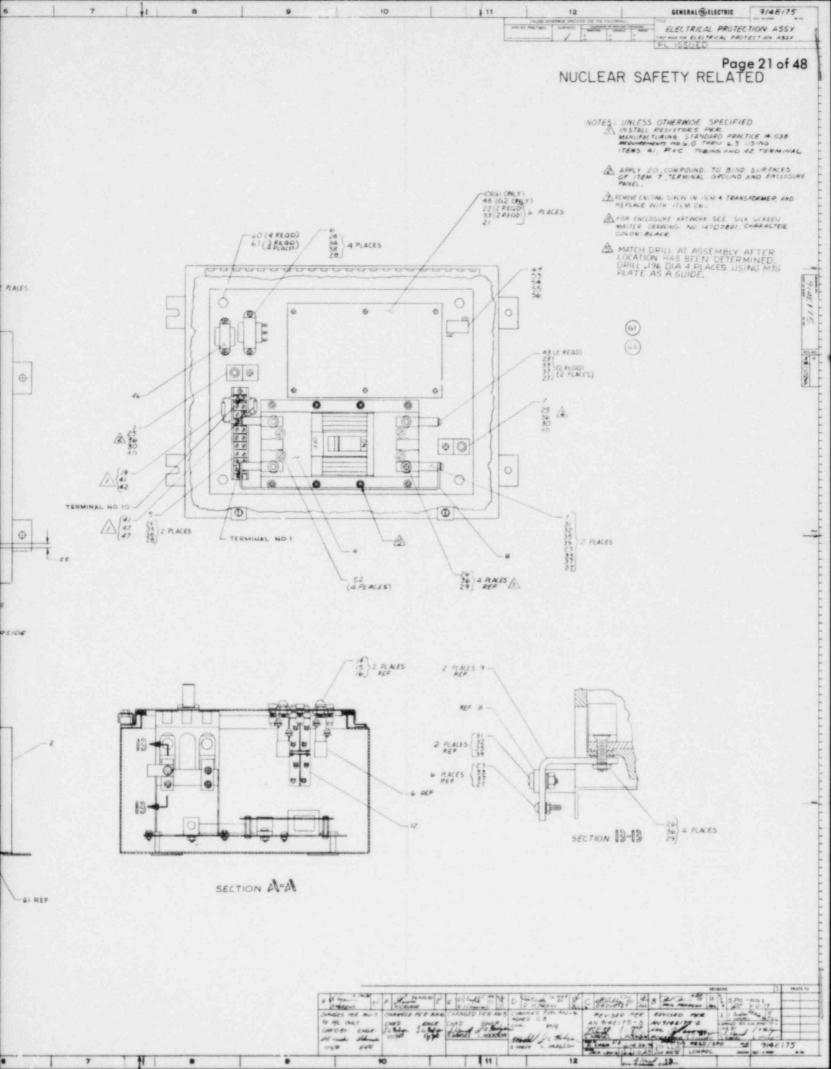
- Open vapor barrier bag with a minimum amount of destruction. (Will be resealed if practical.)
- B. Visually inspect EPA enclosure and components for moisture damage.
- C. If condensation is present, use forced heated air to dry enclosure and/or components. (Heated air shall be no greater than 165°F.)
- D. Replace the vapor barrier bag, and include a new bag (1 lb.) of desiccant.
- E. If using a new vapor barrier bag, install a new humidity indicator in skin of bag.
- F. The vapor barrier bag shall then be reevacuated using a suitable pump and resealed using a leak-proof heat sealing method.

6.0 Delivery

One copy of this storage instruction shall be attached securely to the crate, using a weatherproof envelope.

The weatherproof envelope shall be marked in 1/2 inch bold letters "Seller's Storage Instructions Inside".





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PLATE LIST POR TITLE CONT ON SHEET ? 8H NO.] ASSEMBLY 914E175 ELECTRICAL PROTECTION ASSY FIRST MADE FOR ELECTRICAL PROTECTION ASSY CONT ON BHEET BH MO. PART NAME DRAWING NO., DESCRIPTION, MATERIAL, WEIGHT NO. X 914E175 ELECTRICAL PROTECTION 1 ASSEMBLY 1 ENCLOSURE, MOD 147D7800P001 3 184C4475G001 MOUNT BRACKET 4 18/04/94P1 CIRCUIT BREAKER 1 5 TERMINAL STRIP 219B4600P010 6 TRANSFORMER 219B4603P001 2 7 287A5900P001 TERMINAL, GND 8 BUSS BAR 219B4602P001 q BUSS ANGLE 219B4601P001 10 PCB ASSY 47D523796G001 2 2 11 TIP JACK 287A5901P001 12 164C5274P027, TANDOM BLOCKS SWITCH SWITCH 164C5274P006, CYLINDER LOCK DEVICE AMP INDICATING 14 209A5610G42 NOT ISED 15 NOT 16 287A5902P001, DATA LAMP CARTRIDGE INDICATOR LIGHT 18 LAMP BASE 287A5903P001, DATA LAMP HOLDER 1 19 184C4489P65H2001FR RESISTOR 20 COMPOUND 287A5904P003, PENETROX A SPACER 287A5906P001, #6-32 HEX 1" LONG 14 14 SCREW N80P13008C, PAN HEAD #6-32 UNC X 1/2" 6 SCREW N80P13012C PAN HEAD #6-32 UNC X 3/4" 6 6 SCREW N80P15008C PAN HEAD #8-32 UNC X 1/2" 2 SCREW N80P24012C. PAN HEAD 5/16-24 UNF X 3/4" 4 SCREW N84P23016C 26 FLAT HEAD 5/16-18 UNC X 1" 8 NUT N210P013C #6-32 UNC STI DESCRIPTION OF GROUPS REVISIONS PRINTS TO PICASANO CHANGE PER AN - 6 HANGED PER ANDA The Solar SIZVANE ! TRISCRAFORD HEL CHANGED PER AN-7 CHANGED PETL AN-5 CHKDBY: 877 and Sandody 7 1/14/81 APPROVALE L. HODGE ON teriand PARTS LIST FOR Antito2/12/79 SWEM DO JUNE 26.79 DIV. OR LOMPOC h 13 July 79 WHITE U. LEWIS LOCATION CONT ON SHEET

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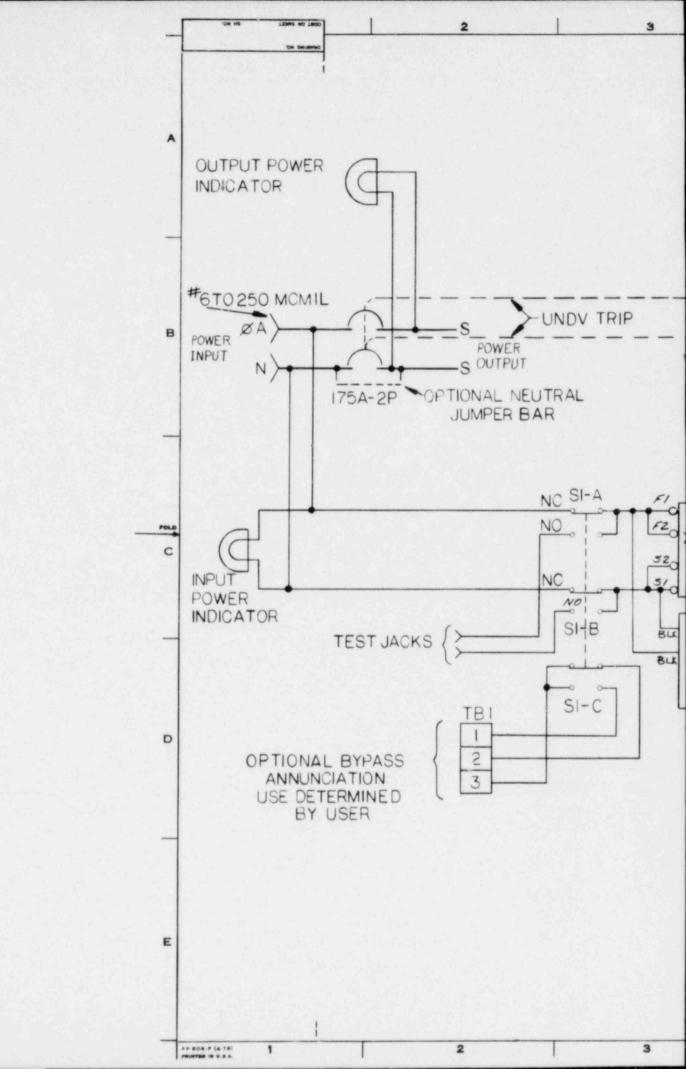
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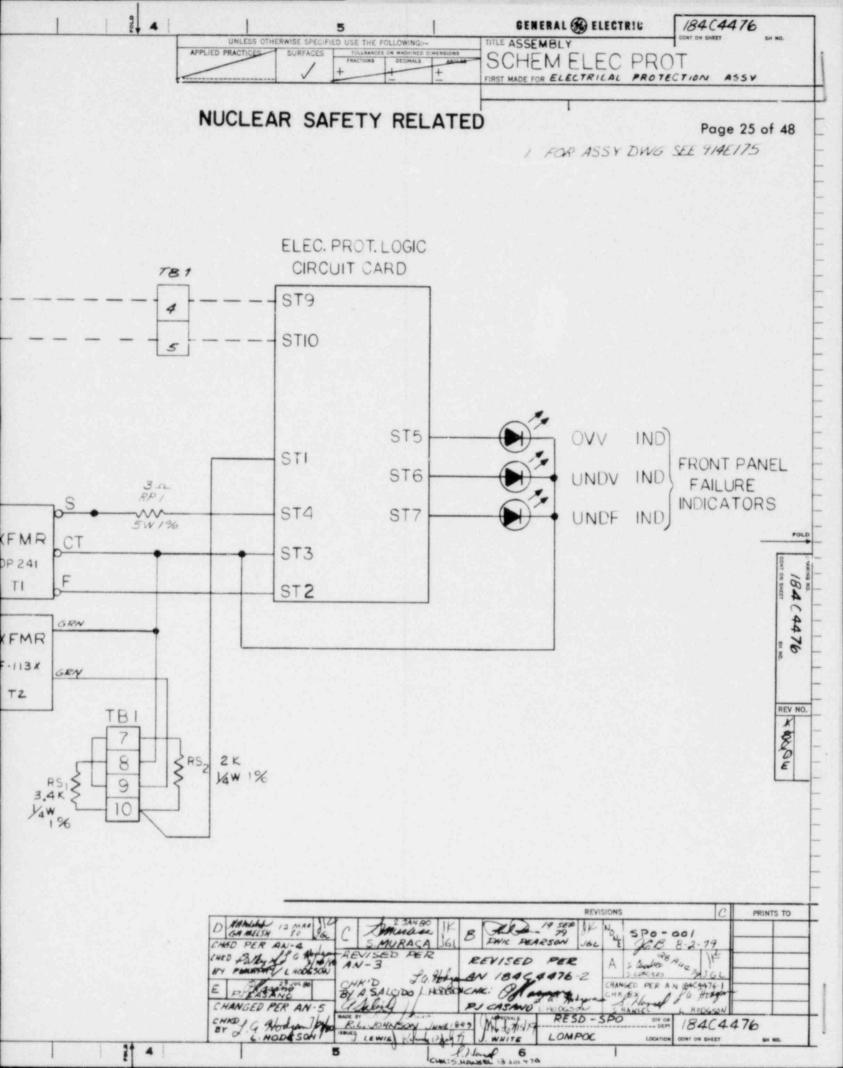
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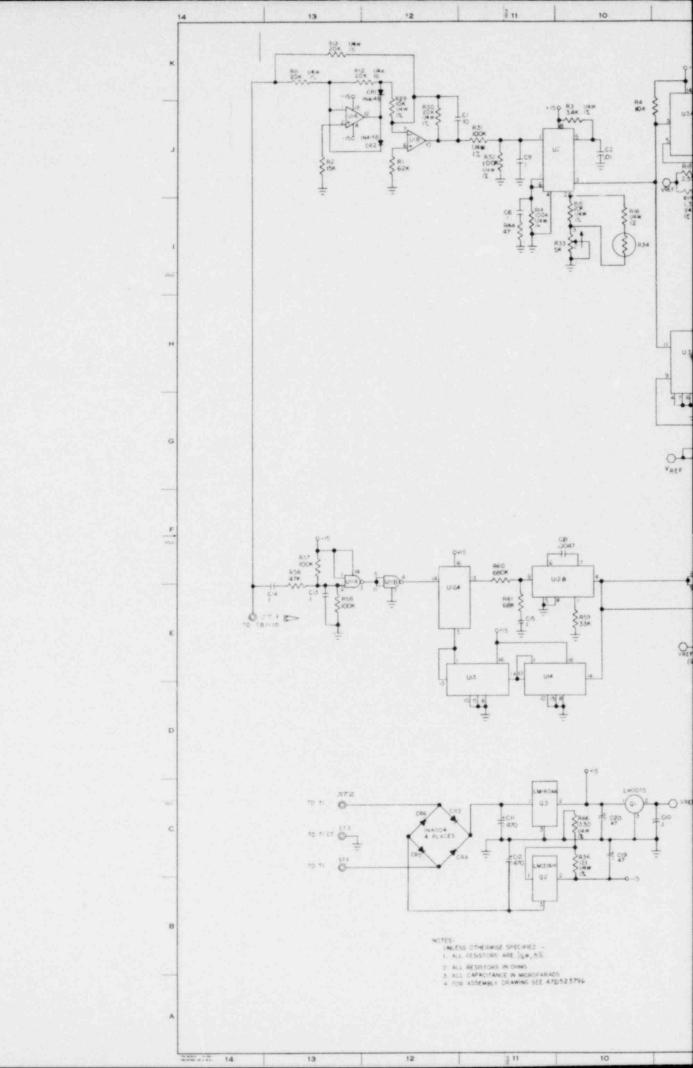
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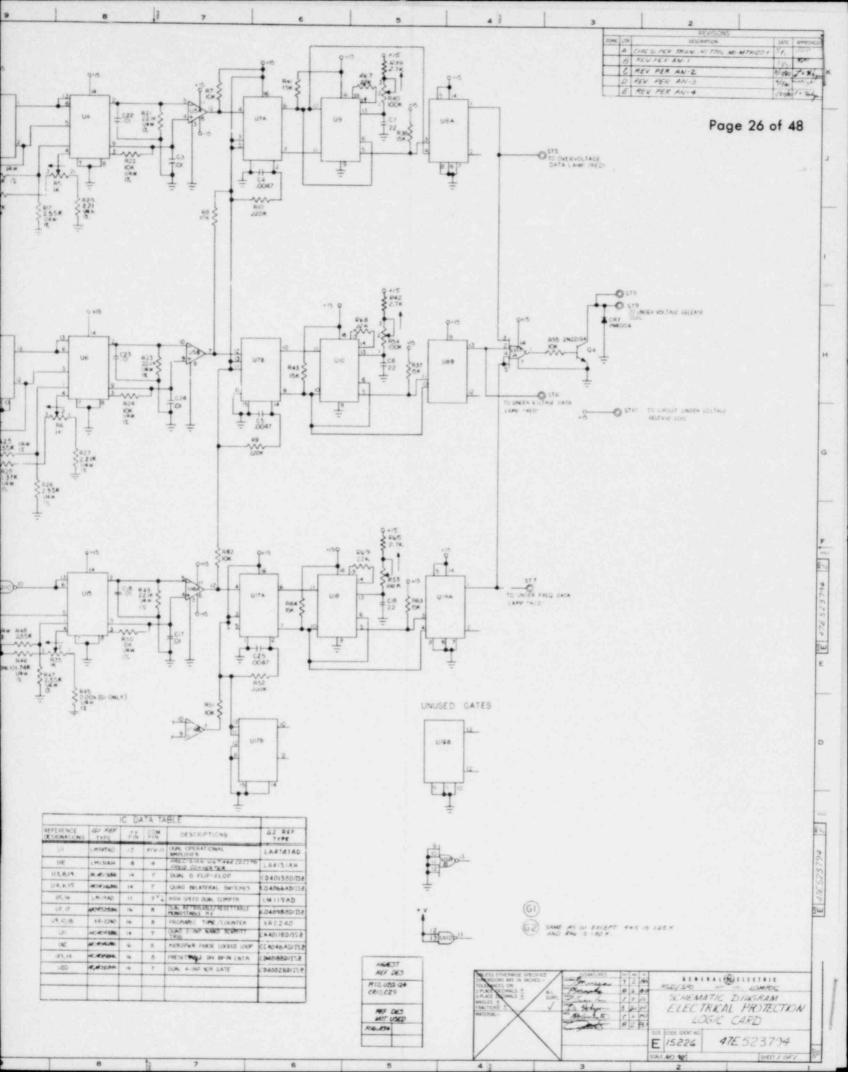
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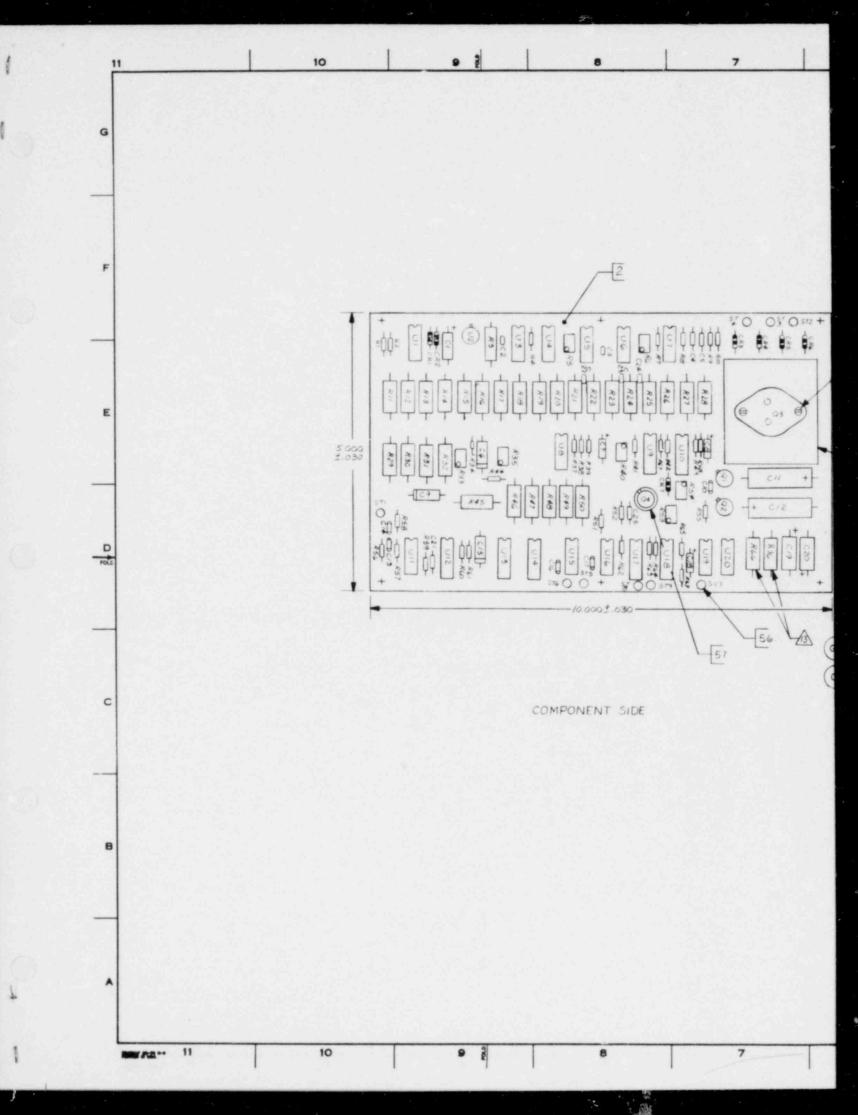
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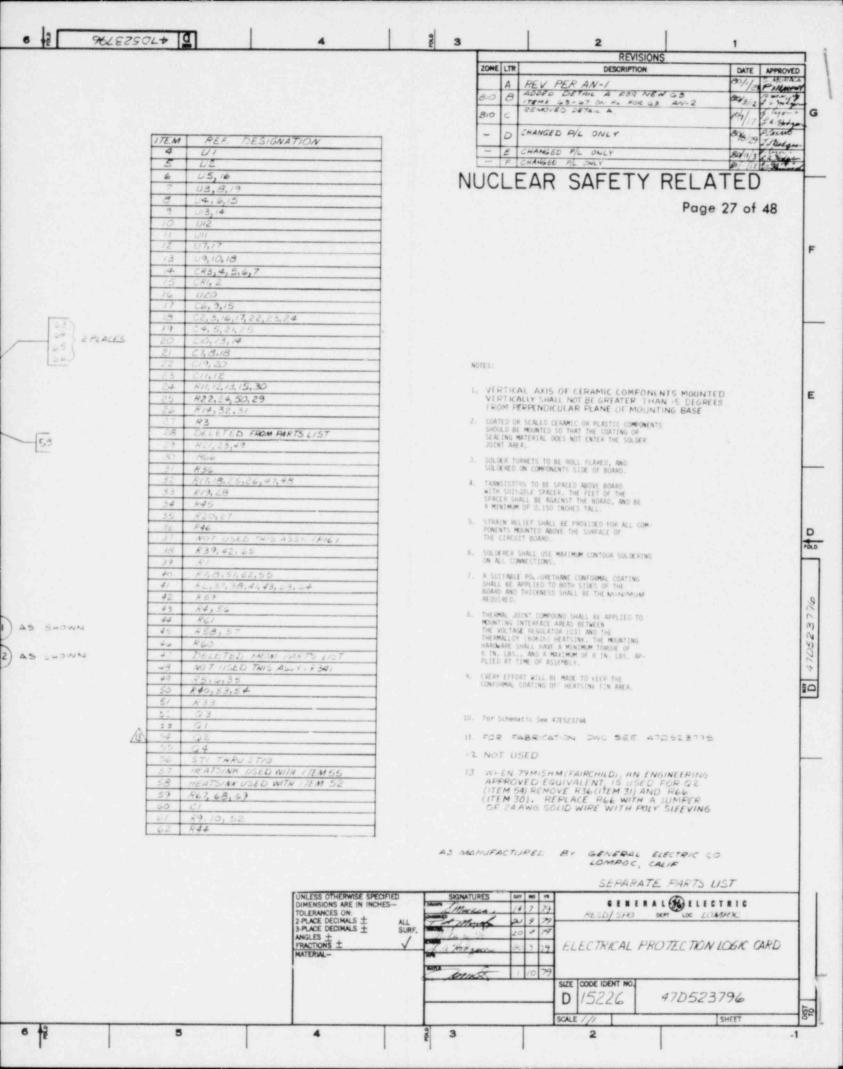












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2			47D523796P1N2	PC BOARD		1				
3			47E523794	SCHEMATIC						
4			184C4477P001	INTEGRATED CIRCUIT U1		1				
5			184C4478P001	INTEGRATED CIRCUIT U2		1				Commence or second
6			134C4479P001	INTEGRATED CIRCUIT U5, U16		2				Contraction of the latest desired
7			169C8671P013	INTEGRATED CIRCUIT U3, U8, U19		3				Contract of the last of the la
8			184C4480P001	INTEGRATED CIRCUIT U4, U6, U15		3				The second liverage of the second
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10			184C4480P003	INTEGRATED CIRCUIT U12		1				
11			169C8671P093	INTEGRATED CIRCUIT U11		1				
12			169C8671P528	INTEGRATED CIRCUIT U7, U17		2				
13			184C4484P001	INTEGRATED CIRCUIT U9, U10, U18		3				
14			176A1572P004	DIODE CR3-CR7		5				
15			175A7485P002	DIODE SWITCHING CR1, CR2		2				
16			169C8671P002	INTEGRATED CIRCUIT U20		1				The second secon
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38			163C1862P272F	CARBON RES. R39, R42, R65		3				The second second
39			163C1862P622F	CARBON RES. R1		1				-
40			163C1862P103F	CARBON RES. R7, R8, R51, R6 R55, R4	2	6				-
41			163C1862P153F	CARBON RES. R2, R37, R38,R4 R43, R63, R64	1	7				-
42			163C1862P333F	CARBON RES. R59		1				-
43			163C1862P473F	CARBON RES. R56		1				-
44			163C1862P683F	CARBON RES. R61		1				-
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56			287 <i>P</i>	A5905P001	TER	MINAL POST			10				
57			2198	34604P001	HEA:	TSINK			1				
58			2198	4605P001	HEAT	TSINK			1				1
59			1630	1862P223F		30N RES. , R68, R69			3				-
60			1120	2642P28	CAP/	ACITOR			1				
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62			1630	1862P470F	CARE R44	BON RESISTOR	R		1				San Stranger Stranger Stranger
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____LOCATION SH NO CONT ON REV TOLE ELECTRICAL PROTECTION CODE IDENT NO PL LOGIC CARD 15226 47D523796G1 REVISION CODES UNIT OF MEASURE CODES DRAWING CODES FOR GE USE ONLY GRAMS PIECES FEET POUNDS GALLONS 5. SETS 6. OUNCES 7. INCHES 8. PINTS 9. OTHER 1. ITEM ADDED 4. IDENT NO CHGD 6 COIT CORRECTION 1. VENDOR ITEM - SPEC CONT DWG 4. SHORT FORM DWG 5. INSEP ASSY 2. ITEM DELETED 5. IDENT NO. AND 2. VENDOR ITEM SOURCE CONTIDWG 6. MICHD PT 3. ALTERED OR SELECTED PT 3. QTY CHGD QTY CHGD 7. CUST FURN PT CODE ITEM DESCRIPTION/ ROUT SHEET ZONE IDENTIFICATION NO. IDENT CODE NOMENCLATURE ING NO. NO. M NO. SCREW 3 N80P9006C 63 $4-40 \times .38$ STEEL 64 N210P009C NUT 4-40 HEX 2 3 STEEL N405P005C 65 LOCKWASHER NO. 4 3 STEEL 66 N402AP005C FLAT WASHER 3 NO. 4 STEEL 67 287A4931P001 PAD, TRANSISTOR 3 PARTS LIST (G.E.)

(4-69)

MADE BY P. Ly U. S. Muraca

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CODE IDENT NO. ELECTRICAL PROTECTION PL 47D523796G2 15226 LOGIC CARD

DRAWING CODES I. VENDOR ITEM SPEC CONT DWG

TITLE

A. SHORT FORM DWG 5. INSEP ASSY 6. MTCHD PT 2. VENDOR ITEM SOURCE CONT DWG

FOR GE USE ONLY

UNIT OF MEASURE CODES

GRAMS PIECES FEET

REVISION CODES 1. ITEM ADDED 4 IDENT NO CHGD 6 EDIT CORRECTION

2 ITEM DELETED 5. IDENT NO. AND

ITEM	ZONE	D'NG CODE	IDENTIFICATION NO.	DESCRIPTION/ NOMENCLATURE	IDENT NO	QTY	UVA	ROUT	SHEET	0.0
1			47D523796G2	ASSEMBLY						
2			47D523796P1N2	PC BOARD		1				
3			47E523794	SCHEMATIC						
4			184C4477P001	INTEGRATED CIRCUIT		1				-
5			184C4478P001	INTEGRATED CIRCUIT U2		1				-
6			184C4479P001	INTEGRATED CIRCUIT U5, U16		2				
7			184044019004	INTEGRATED CIRCUIT U3, U8, U19		3				Section 1999
8			184C4481P005	INTEGRATED CIRCUIT U4, U6, U15		3				-
9			184C4481P003	INTEGRATED CIRCUIT U13, U14		2				
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										Annual Control of Street, or other Persons or other Perso
										Section Section Section Section
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DE BY D.	/		DATE ISSUED REVISION AUTHOR	-1-1-1- PL 4				SH NO.	CONT ON SHEET	13

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LOMPOC, CALIFORNIA

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SH NO. CONT ON REV TITLE CODE IDENT NO ELECTRICAL PROTECTION PL 15226 47D523796G2 LOGIC CARD REVISION CODES DRAWING CODES UNIT OF MEASURE CODES FOR GE USE ONLY I. ITEM ADDED 4. IDENT NO. CHGD 6. EDIT CORRECTION 4. SHORT FORM DWG T. VENDOR ITEM SPEC CONT DWG 2. VENDOR ITEM SOURCE CONT DWG 2. ITEM DELETED 5. IDENT NO. AND

	OR SELECTE	-		7. CUST FURN PT	4. GALLONS	9. OTHER	3. QTY CHGD	GTY CHG	Tu			-
NO.	ZONE	CODE		DENTIFICATION NO.		DESCRIPTION/ NOMENCLATURE	IDENT NO	QTY	M	ROUT	SHEET NO.	
10			18404	481P006	INT U12	EGRATED CIRC	UIT	1				
11			18404	481P002	INT U11	EGRATED CIRC	UIT	1				
12			18404	481P007		EGRATED CIRC U17	UIT	2				
13			184C4	484P001		EGRATED CIRC U10, U18	UIT	3				
14			176A1	572P004	DIO CR3	DE - CR7		5				
15			175A7	485P002		DE SWITCHING , CR2		2				
16			18404	4812001	INTI U20	EGRATED CIRC	UIT	1				
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2. ITEM DELETED 5. IDENT NO. AND

NO.	ZONE	DWG CODE	IDENTIFICATION NO.	DESCRIPTION/ NOMENCLATURE	CODE IDENT NO.	QTY	M	ROUT- ING	SHEET NO.	800
17			184C4483P037	CAPACITOR C6, C9, C15		3				
18			184C4482P058	CAPACITOR C2, C3, C16, C17 C22, C23, C24		7				
19			157C4540P472	CAPACITOR C4, C5, C21, C25		4				
20			184C4492P030	CAPACITOR C10, C13, C14		3				
21			184C4491P4-2283	CAPACITOR C7, C8, C18		3				
22			112C264222-2295	CAPACITOR C19, C20		2				
23	- 1		184C4493P008	CAPACITOR C11, C12		2				_
										-
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__LOCATION SH NO. CODE IDENT NO. ELECTRICAL PROTECTION PL 15226 47D523796G2 LOGIC CARD REVISION CODES UNIT OF MEASURE CODES DRAWING CODES FOR GE USE ONLY 4. SHORT FORM DWG 5. INSEP ASSY I ITEM ADDED 4 IDENT NO. CHGD 6 EDIT CORRECTION 5. SETS 6. OUNCES 7. INCHES O. GRAMS
I. PIECES
2. FEET 1. VENDOR ITEM SPEC CONT DWG 2 ITEM DELETED 5 IDENT NO AND 2 VENDOR ITEM - SOURCE CONT DWG

ALTERED (OR SELECTE	D PT		6. MTCHD PT): CUST FURN PT	3. POUNDS 4. GALLONS	8. PINTS 9. OTHER	3.0	CODE	QTY CHG	U			
NO.	ZONE	CODE	11	DENTIFICATION NO.		DESCRIPTION/ NOMENCLATURE		NO.	QTY	M	ROUT	SHEET NO.	
24			18404	489P65H2002FR	RES R11	AL FILM ISTOR , R12, R13 , R30			5				
25			18404	489P 6 5H1002FR	RES	AL FILM ISTOR , R24, R53,	020		4				
26			184C4	489P65H1003FR	RES	AL FILM ISTOR , R32, R31			3				
27			18404	489P65H3401FR		AL FILM ISTOR			1				
28					NOT	USED							
29			184C4	489P65H2212FR	RES	AL FILM ISTOR , R23, R49			3				the state of the s
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_ LOCATION ONT ON REV CODE IDENT NO SH NO. ELECTRICAL PROTECTION 47L523796G2 E 15226 LOGIC CARD 6 REVISION CODES UNIT OF MEASURE CODES DRAWING CODES FOR GE USE ONLY 5. SETS
6. OUNCES
7. INCHES
8. PINTS
9. OTHER I, ITEM ADDED & IDENT NO. CHGD 6. EDIT CORRECTION O. GRAMS
1. PIECES
2. FEET
3. POUND I. VENDOR ITEM SPEC CONT DWG 4. SHORT FORM DWG S INSEP ASSY 2 ITEM DELETED 5 IDENT NO. AND 2. VENDOR ITEM SOURCE CONT DWG 6 MTCHD PT 7 CUST FURN PT POUNDS 3. ALTERED OR SELECTED PT 3. QTY CHGD QTY CHGD 4. GALLOWS DESCRIPTION/ ROUT SHEET ITEM DWG QTY IDENTIFICATION NO. NO. ZONE NOMENCLATURE NO 30 184C4489P65H1331FR METAL FILM RESISTOR R66 31 184C4489P65H1210FR METAL FILM 1 RESISTOR R36 32 184C4489P65H2551FR METAL FILM 6 RESISTOR R17, R18, R25, R26, R47, R48 33 184C4489P65H1371FR METAL FILM RESISTOR R19, R28 34 184C4489P65H1651FR METAL FILM RESISTOR R45 35 184C4489P65H2211FR METAL FILM 2 RESISTOR R20, R27 36 184C4489P65H1501FR METAL FILM RESISTOR R46 37 NOT USED S. Muraca FOR 29 01 80 REVISION AUTHORITY SH NO REV CONT ON SHEET

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THE ELECTRICAL PROTECTION LOGIC CARD

CODE IDENT NO. 15226

47D523796G2

6

DRAWING CODES

1. VENDOR ITEM SPEC CONT DWG

FOR GE USE ONLY 4. SHORT FORM DWG

UNIT OF MEASURE CODES GRAMS 5. SETS PIECES 6. OUNCES

REVISION CODES

1. ITEM ADDED 4. IDENT NO. CHGD 6 EDIT PORRECTION

NO.	ZONE	CODE	IDENTIFICATION NO.	DESCRIPTION/ NOMENCLATURE	IDENT NO.	QTY	M	ROUT	SHEET	-
38			163C1862P272F	CARBON RES.		3				
39			163C1862P622F	CARBON RES. R1		_1	Second Second			
40			163C1862P103F	CARBON RES. R7, R8, R51, R62 R55, R4		6				
41			163C1862P153F	CARBON RES. R2, R37, R38, R41 R43, R63, R64		7				
42			163C1862P333F	CARBON RES. R59		1				
43			163C1862P473F	CARBON RES. R56		1				
44			163C1862P683F	CARBON RES. R61		1				
			DATE ISSUED REVISION AUTHORITY							

LOMPOC, CALIFORNIA

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CONT ON REV ____DEPT SH NO TITLE CODE IDENT NO. ELECTRICAL PROTECTION PL 5 LOGIC CARD 47D523796G2 15226 REVISION CODES DRAWING CODES UNIT OF MEASURE CODES FOR GE USE ONLY GRAMS PIECES FEET POUNDS GALLONS 5. SETS 6. OUNCES 7. INCHES 8. PINTS 9. OTHER I. ITEM ADDED 4. IDENT NO. CHGD 6. EDIT CORRECTION 4. SHORT FORM DWG I VENDOR ITEM SPEC CONT DWG 2. VENDOR ITEM SOURCE CONT DWG 2. ITEM DELETED 5. IDENT NO. AND

ITEM	ZONE	DWG	IDENTIF	CATION NO.		DESCRIPTION/		CODE	QTY	U	ROUT	SHEET	800
NO.		CODE			^	IOMENCLATURE	-	NO.		M	ING	NO.	+
45			163C1862F	104F	CARBO	N RES. R58			2				and and the same of the party of
46			163C1862F	684F	CARBO R60	N RES.			1				Section of the second
47					NOT U	SED							-
48					NOT U	SED							1
49			184C4490F	009	RESIS R5, R	TOR, VARIA	ABLE		3				
50			184C4490F	017	RESIS R40,	TOR, VARIA	ABLE		3				Section of the least of the lea
51			184C4490P	012	RESIS R33	TOR, VARIA	ABLE		1				-
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LOCATION CONT ON REV

HITE ELECTRICAL PROTECTION LOGIC CARD

CODE IDENT NO. 15226

UNIT OF MEASURE CODES

47D523796G2

F 8 9

DRAWING CODES

1. VENDOR ITEM SPEC CONT DWG VENEOR ITEM. SOURCE CONT DWG

FOR GE USE ONLY 4 SHORT FORM DWG

GRAMS

5. SETS 6. OUNCES 7. INCHES

PL

REVISION CODES I. ITEM ADDED 4. IDENT NO. CHGO 6. EDIT CORRECTION

2. ITEM DELETED 5. IDENT NO. AND

ITEM	ZONE	DWG	10	DENTIFICATION NO.	DESCRIPTIO(4/	CODE	QTY	U	ROUT	SHEET NO.	
NO.		CODE			NOMENCLATURE	NO.	-	W	ING	NO.	
52			18404	4485P001	TRANSISTOR Q3		1				
53			21984	1607G001	VOLTAGE REFERE ENCAPSULATED		1				
54			18404	1486P001	TRANSISTOR Q2		1				
55			18404	4488P001	TRANSISTOR Q4		1				
56			287A5	905P001	TERMINAL POST		10				
57			21984	604P001	HEATSINK		1				
58			21984	605P001	HEATSINK		1				
59			16301	862P223F	CARBON RES. R67, R68, R69		3				
60			11202	2642P028	CAPACITOR C1		1				
61			16301	862P224F	CARBON RESISTO R9, R10, R52	R	3				
62			16301	862P470F	CARBON RESISTO R44)R	1				
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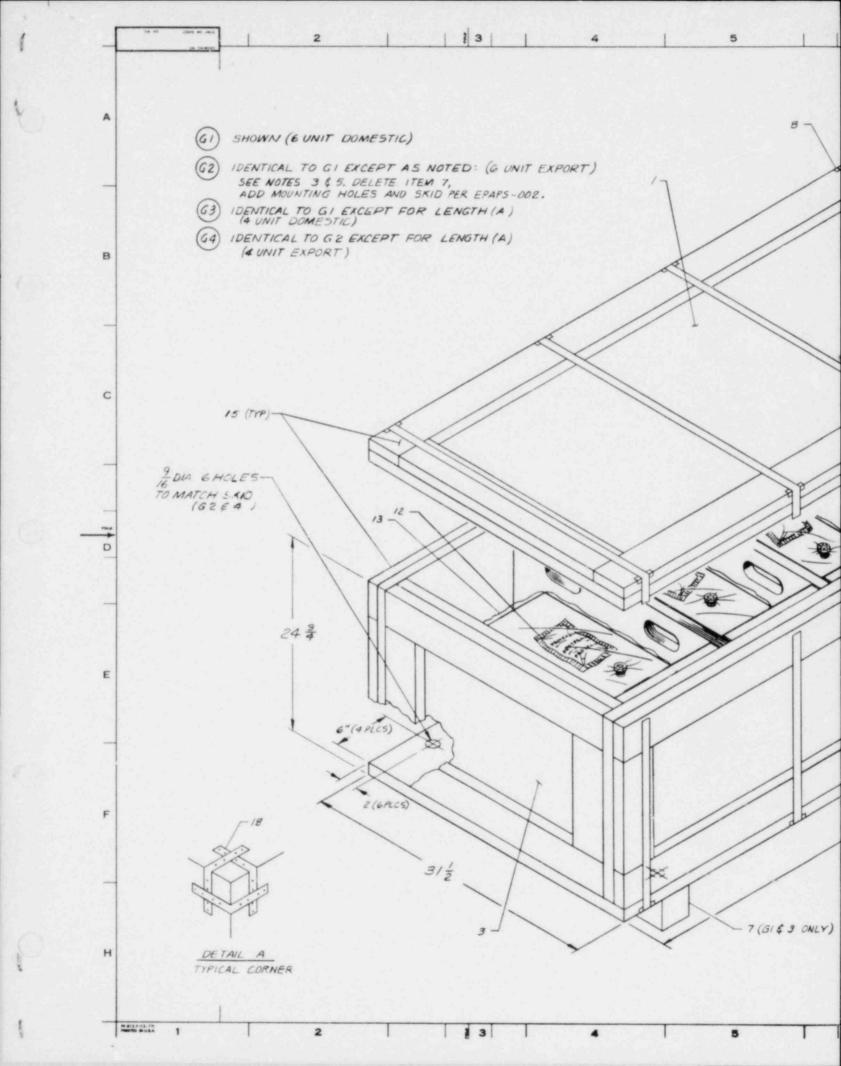
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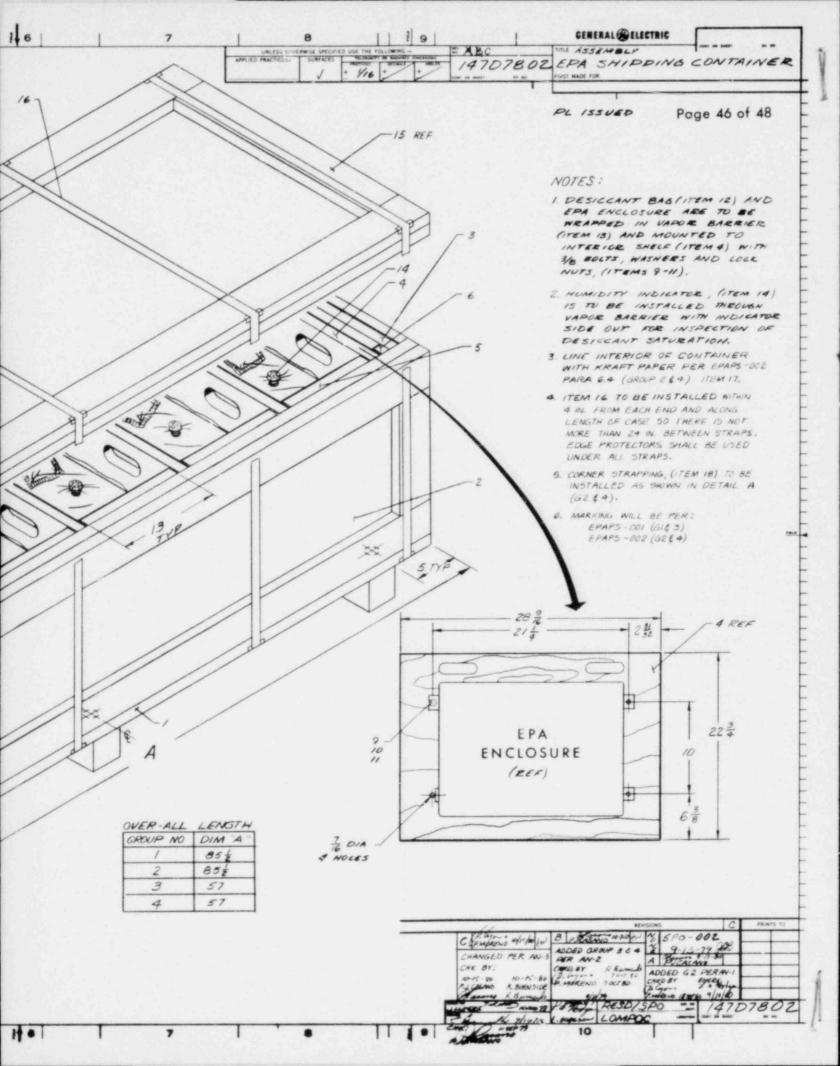
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PL E 15226 47D523796G2 LOGIC CARD UNIT OF MEASURE CODES DRAWING CODES REVISION CODES FOR GE USE ONLY O. GRAMS
1. PIECES
2. FEET
3. POUR 5. SETS 6. OUNCES 7. INCHES 8. PINTS 9. OTHER 1. ITEM ADDED 4. IDENT NO. CHGD 6 EDIT CORRECTION 1. VENDOR ITEM SPEC CONT DWG 4. SHORT FORM DWG INSEP ASSY 2. ITEM DELETED 5. IDENT NO. AND. 2. VENDOR ITEM SOURCE CONT DWG MTCHD PT 3. POUNDS 4. GALLONS 3. ALTERED OR SELECTED PT QTY CHGD 3. QTY CHGD CUST FURN PT CODE DESCRIPTION/ SHEET ROUT ITEM DWG QTY IDENTIFICATION NO. ZONE IDENT CODE NO. NOMENCLATURE NO. M NO. 63 N80P9006C SCREW, PAN HEAD 4-40 x .38 2 64 N210P009C NUT HEX, 4-40 STEEL 65 N405P005C LOCKWASHER, NO.4 STEEL 66 N402AP005C WASHER, FLAT NO.4 2 STEEL 67 287A4931P001 PAD, TRANSISTOR 3 (4-69) PARTS LIST (G.E.) MADE BYP. Wr. DATE ISSUED REVISION AUTHORITY SH NO. CONT ON SHEET REV S. Muraca Fra 29 47D523796G2 9 F CONTRACT NO. DISTRIBUTION CODE OR





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TITLE ASSEMBLY EPA SHIPPING CONTAINER 14707802 FIRST MADE FOR PART NAME DRAWING NO., DESCRIPTION, MATERIAL, WEIGHT G4 G3 NO. IN THE EXT GRADE PLYWOOD 2 2 TOP, BTM COVER 1 2 2 2 M IN THE EXT GRADE PLYWOOD SIDE COVER ME IN THE EXT GRADE PLYWOOD END COVER TO IN THE EXT GRADE PLYWOOD 6 INT SHELF 6 4 12 12 SHELF SPACER IN THE EXT GRADE PLYWOOD 2 6 SHELF SPACER IN THE EXT GRADE PLYWOOD 4.00 x 4.00 STRAIGHT GRAIN FIR * 2046TIZ MCMASTER AR AR AR AR EDGE PROTECTOR 24 24 9 BOLT N18P25018B 24 24 WASHER, FLAT N402AP13B NUT, LOCK N264P25B MIL-D-3464, Type II, Non-Dusting DESICCANT BAG AR AR AR AR 13 VAPOR BARR *AEC B28596 Humidial Co. 6 6 14 HUMIDITY IND 15 AH AR AR AR NAILER .75 x 3.50 Straight Grain Fir 75 x .023 Min. AR BANDING, STEEL AR 16 AR 17 AL KRAFT PAPER PPP-B-1055 CLASS L-2b CORNER STRAPPING 75 : .023 IIN. 15 8 18 *Buyer Approved Equiv. PRINTS TO DESCRIPTION OF GROUPS REVISIONS. Parano ano 10.7.80 km 9-13-79 KB 10-15-80 W APDED GR 384 PER AN.Z CHANGED PER AN-3 BASANO 91780 CHKB BY CHKD BY: 10-15-80 P.MORENO 10CT 80 ADDED GZ PER AN-I 10-15-80 CHKO BY P.J. CASANO R. BURNSIDE 7.6,000 PMORENO 18 SEP 80 P. PEARSON 22 MB 79 14707802 LUMPOC LOCATION CONT ON SHEET SH NO.

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