



LER SUPPLEMENTAL INFORMATION

BFRO-50- 259 / 8033 Technical Specification Involved 3.5.B.4 and 3.5.B.6  
Reported Under Technical Specification 6.7.2.b (2)  
Date of Occurrence 4/17/80 Time of Occurrence 0518 Unit 1

Identification and Description of Occurrence:

480 V reactor MOV board 1E tripped and locked out on overcurrent. Loop II of LPCI was made inoperable for 4 minutes.

Conditions Prior to Occurrence:

Unit 1 @ 95%

Unit 2 @ 80%

Unit 3 @ 98%

Action specified in the Technical Specification Surveillance Requirements met due to inoperable equipment. Describe.

Loop I of LPCI RHR pump C was operable, RHR pump A was tagged out for heat exchanger repair. Loop II was also capable of manual alignment for operation.

Apparent Cause of Occurrence:

The event was due to a short circuit on the hot side of spare breaker compartment 3A. The molded case breaker was wired such that the primary wires were compressed between the breaker and the compartment. The compressed insulation made it possible for the conductors to become exposed.

Analysis of Occurrence:

There was no damage to plant equipment. There was no activity release, no personnel exposure or injury and danger to health or safety of the public.

Corrective Action:

The fault was cleared and the board reenergized and loop II of LPCI returned to service. The breaker and associated wiring was then replaced with same model and manufacturer. A random check of other compartments was made and no similar problems found. A procedure for verification that this problem does not exist on other CSSC boards is being prepared.

Failure Data:

NA

\*Retention: Period - Lifetime; Responsibility - Administrative Supervisor

\*Revision: 