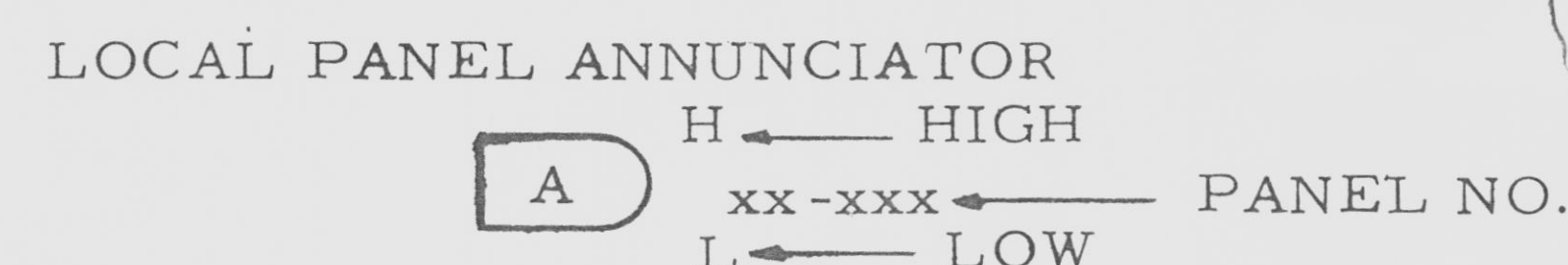
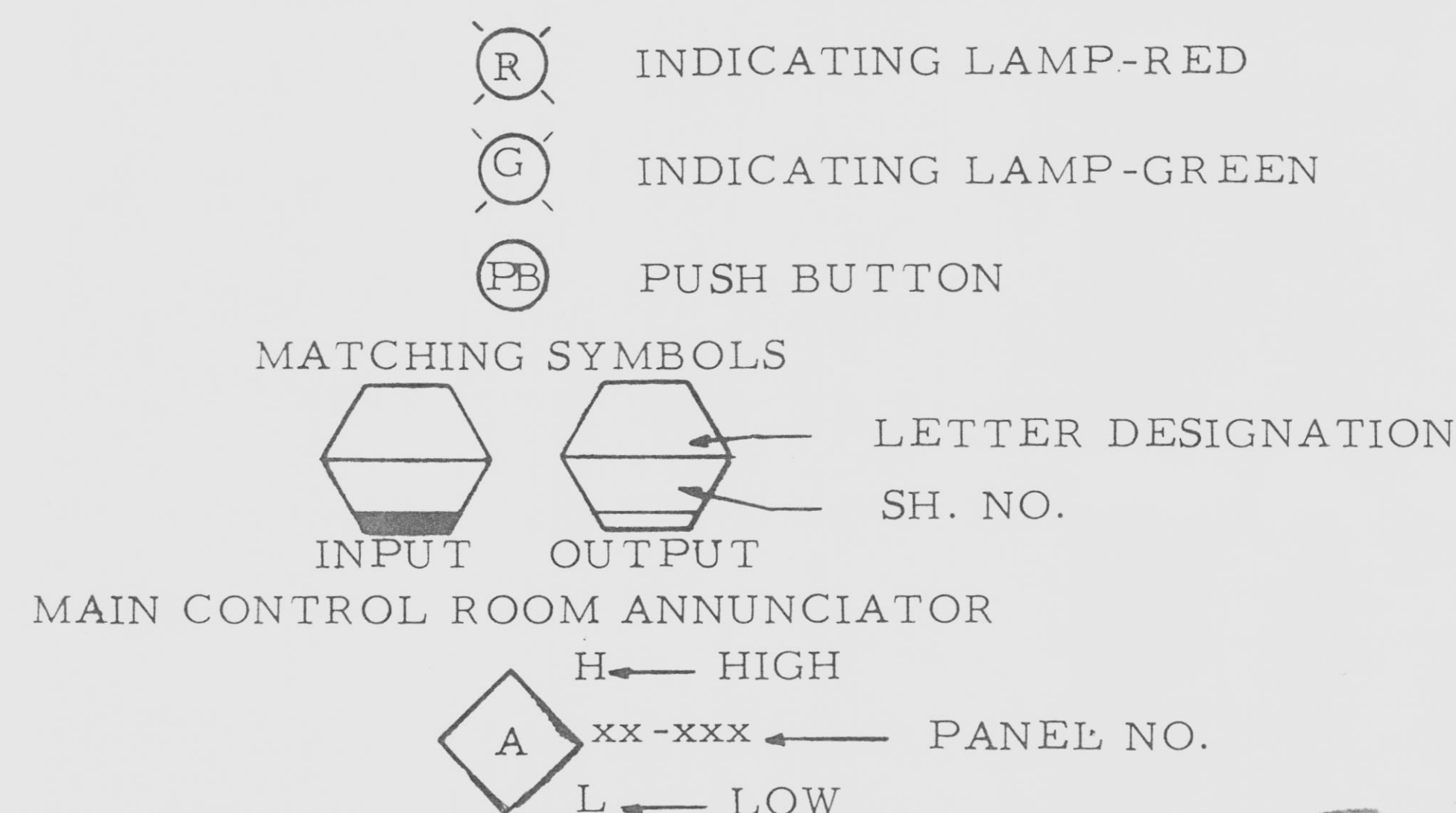
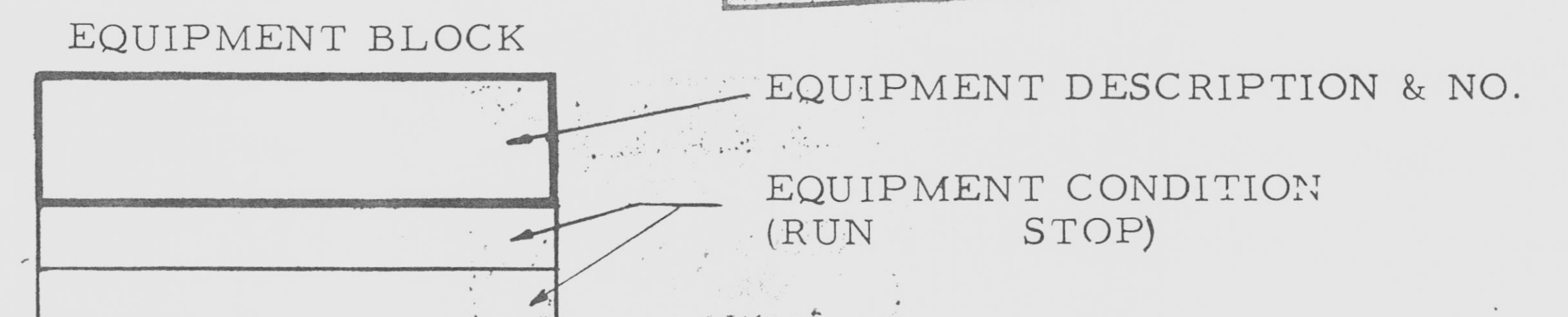
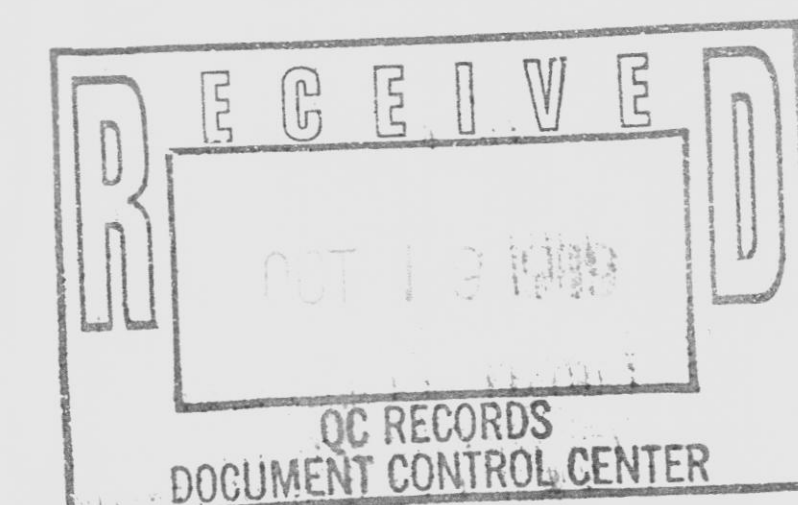
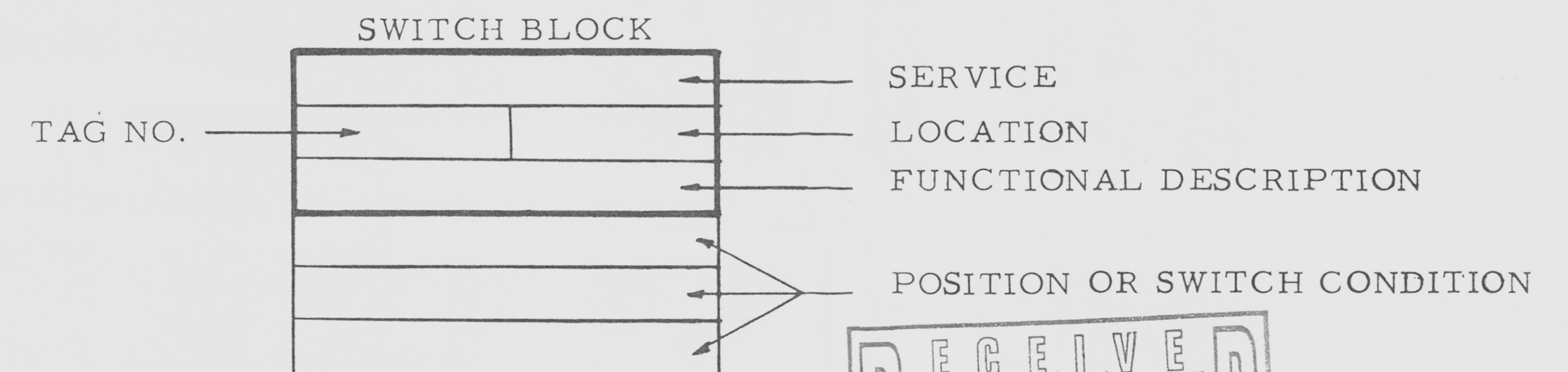
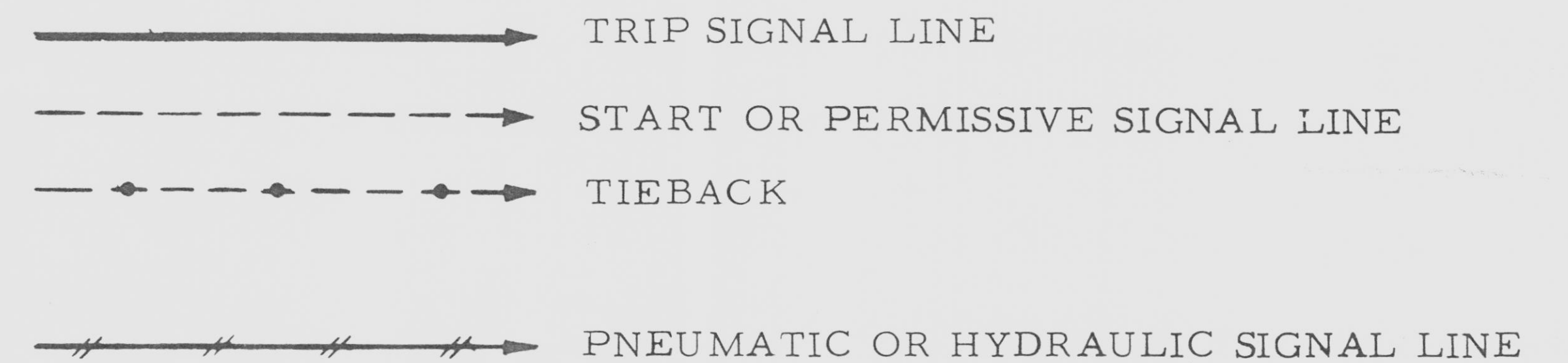


LOGIC DIAGRAM LEGEND

SYMBOL	DESIGNATION	ACTION
	AND GATE	GIVES OUTPUT ONLY IF ALL INPUTS ARE PRESENT
	OR GATE	GIVES OUTPUT IF ANY INPUT IS PRESENT
	NOT GATE	GIVES OUTPUT ONLY IF INPUT IS NOT PRESENT
	TIME DELAY UNIT	PRODUCES OUTPUT SAME AS INPUT AFTER DELAY (SEC.)
	MEMORY UNIT	A DEVICE WHICH RETAINS ITS STATE UNTIL REQUESTED TO ALTER ITS STATE
	TIME DELAY (WIPE OUT) UNIT	OUTPUT CHANGES ON APPLICATION OF INPUT AND IS WIPE OUT AFTER SECS.



FOR INFORMATION ONLY

RI-BFI 130-1

REV. NO. 1 DATE 6-23-97

EBASCO SERVICES INCORPORATED
NEW YORK

FLORIDA POWER & LIGHT COMPANY
HUTCHINSON ISLAND PLANT-UNIT I
1973-890MW INSTALLATION

DIV. 14C DR. AZ
SCALE 1/4" CH. CA
DATE DEC 14, 1971

APPROVED
[Signature]

CONTROL & BLOCK DIAGRAM

8770
B-276
SHOD.2

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