

September 10, 1990

Docket Nos. 50-250
and 50-251

Mr. J. H. Goldberg
Executive Vice President
Florida Power and Light Company
Post Office Box 14000
Juno Beach, Florida 33408-0420

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Dear Mr. Goldberg:

SUBJECT: TURKEY POINT UNITS 3 AND 4 - REQUEST FOR ADDITIONAL INFORMATION ON
EMERGENCY POWER SYSTEM ENHANCEMENT PROJECT (TAC NOS. 69023 AND 69024)

The purpose of this letter is to request additional information regarding the
Emergency Power System (EPS) Enhancement Project.

The staff has reviewed your submittals in support of the Emergency Power
System Enhancement Project. The review indicates that the staff requires
additional information described in the enclosure. Questions Nos. 1 - 4 were
discussed by telephone with Dean Baker of your staff on August 27, 1990.
Question No. 5 was discussed by telephone with Tom Grozan of your staff on
September 5, 1990. We request that you provide the responses by September 24,
1990, so that the staff can complete its review in a timely manner.

The reporting and/or recordkeeping requirements contained in this letter
affect fewer than 10 respondents; therefore, OMB clearance is not required
under P.L. 96-511.

Any questions you may have concerning the questions should be directed to me

Sincerely,

(Original Signed By)

Gordon E. Edison, Sr. Project Manager
Project Directorate II-2
Division of Reactor Projects-I/II
Office of Nuclear Reactor Regulation

Enclosure:
As stated

cc w/enclosure:
See next page

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| NAME | : <i>[Signature]</i> | : GEdison:s1 | : HBerkow | : | : |
| DATE | : 9/10/90 | : 9/10/90 | : 9/10/90 | : | : |

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Mr. J. H. Goldberg
Florida Power and Light Company

Turkey Point Plant

cc:

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ENCLOSURE

REQUEST FOR ADDITIONAL INFORMATION
REGARDING EMERGENCY POWER SYSTEM ENHANCEMENT PROJECT

The following questions relate to the design of the electrical load sequences.

1. Provide a detailed description of the device(s) used to accomplish electrical isolation, and describe the specific testing performed to demonstrate that the device is acceptable for its application(s). This description should include elementary diagrams to indicate the test configuration and how the maximum credible faults were applied to the device(s).
2. Provide data to verify that the maximum credible faults applied during the test were the maximum voltage/current to which the device could be exposed, and define how the maximum voltage/current was determined.
3. Provide data to verify that the maximum credible fault was applied to the output of the device in the transverse mode (between signal and return) and to verify that other faults were considered, i.e. open and short circuits.
4. Define the pass/fail acceptance criteria for each type of device.
5. Provide a discussion of protective devices or other means provided to prevent lightning strikes or switching surges from propagating to the solid state programmable logic load sequencers and causing a common mode failure of the load sequencers.

