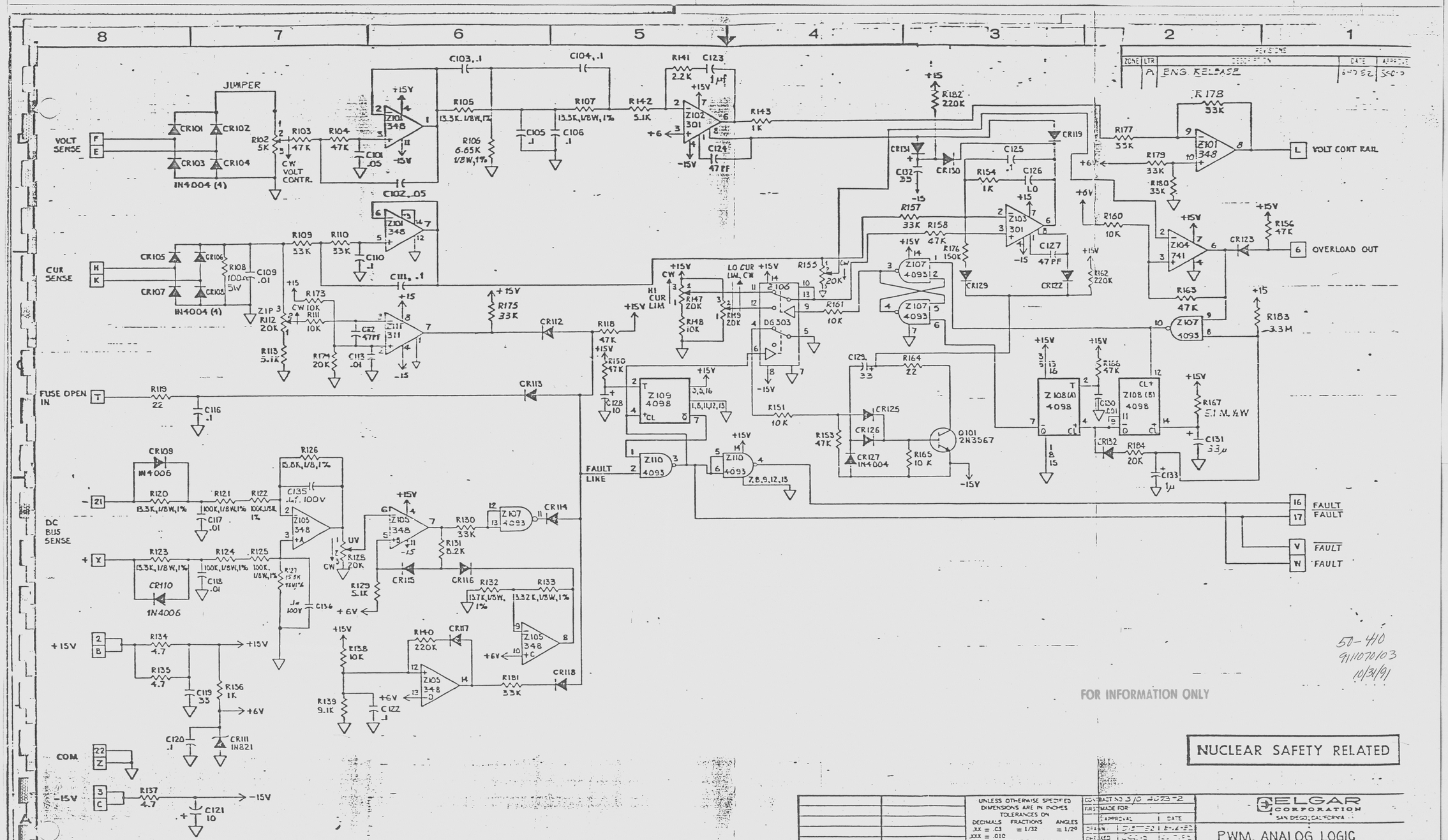

NOTES: UNLESS OTHERWISE SPECIFIED
1. ALL DIODES ARE IN914.
2. ALL RESISTORS ARE 1/4 W, 5%, VALUES.
3. CAPACITOR VALUES ARE IN MICROFARADS.

REVISIONS

ZONE	LTR	DESCRIPTION	DATE	APPROVED
	A	ENG. RELEASE	6-17-82	[illegible]

50-410
9111070103
10/31/91

FOR INFORMATION ONLY

NUCLEAR SAFETY RELATED

UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES
TOLERANCES ON
DECIMALS .XX = .03 .XXX = .010
FRACTIONS = 1/32
ANGLES = 1/2°
DO NOT SCALE THIS DRAWING

MATERIAL:

FINISH:

NEXT ASSY | USED ON
APPLICATION

CONTRACT NO 510 4073-2
FIRST MADE FOR
APPROVAL | DATE
DRAWN
CHECKED
PROJ ENG
QA REL

ELGAR CORPORATION
SAN DIEGO, CALIFORNIA

PWM, ANALOG LOGIC
UPS 103-1-176

SIZE	CODE IDENT NO	DRAWING NO
D	25965	644-014-60

SCALE — SHEET 1 OF 1

E

93050701 81

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