



J1 PIN	FUNCTION
12	CONN. TO THERMOSTAT
11	
10	120 VAC FAN POWER APPROX 3AMP.
9	
8	FAN FAILED LOGIC OUTPUT (1=FAILED)
7	SPARE
6	LEG COMMAND
5	LEG DISCONNECT
4	SPARE
3	+20VDC
2	LOGIC GROUND
1	-20VDC

JACK (BOARD EDGE)

AI GATE DRIVE CARD B/M 101 072 194

SI APERTURE CARD

9305060093 D

C110611321

9305060093

REVISIONS				SCHEMATIC DIAGRAM			
REV.	DESCRIPTION	DATE	BY	A.C.L. - 30/60KW (B/M 101 618 958)			
B	CN7343	5-1-80	PTH	Tolerances Dec			
A	CN5144-2	7/23/77	PDC	1 Place Decimals ± .1 Angles			
1	RN5562-12	1/9	AP	2 Place Decimals ± .05 2 OF 3P			
2				3 Place Decimals ± .010			
These drawings and specifications are the property of IBM Corporation and shall not be reproduced or copied, or used in any form for manufacturing or other purposes without the written permission of IBM Corporation.				ON WAR 11/19/77 RETURN PDC 2/14/77 P. COREY 2/14/77 0000			

C110 611 321

07-558-91

50-410  
911070#03  
10/31/78