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I. DESCRIPTION OF EVENT

On February 2, 1987 at 0516 with the reactor mode switch in Shutdown (operational condition 4), and the reactor at 0% power and at 142 degrees Fahrenheit and 600 pounds per square inch gauge (during a hydrostatic test), the main steam isolation valves (MSIVs) closed in response to an isolation signal. This incident occurred as a result of two safety related reactor monitoring devices being tripped during performance of an operability surveillance test on the reactor instrument line excess flow check valves. The inadvertent tripping of these devices satisfied the logic to isolate the MSIV's.

The sequence of events are as follows: (Refer to Figure 1)

On February 2, 1987 at 0150 while performing surveillance on excess flow check valve 2ISC*EFV10, level transmitter 2ISC*LT11B momentarily tripped on a low-low-low (level 1) reactor vessel (RPV) water level. (This signal was a byproduct of the surveillance testing and not representative of the actual RPV water level. The actual RPV water level was being maintained at a high level point which was greater than level 8.) This momentary trip, which automatically cleared four seconds later, caused one channel of the MSIV trip logic to seal in. The MSIV's did not close at this time.

At 0516 the same day, while performing surveillance on excess flow check valve 2ISC*EFV15, level transmitter 2ISC*LT11C momentarily tripped on a low-low-low (level 1) RPV water level. (This signal was also a byproduct of testing and not representative of the actual RPV water level). This second device trip in conjunction with the earlier trip satisfied the logic to isolate the MSIV's.

There were no other inoperable systems that contributed to this event.

II. CAUSE OF EVENT

There are two root causes for this event: (1) procedure deficiency and, (2) possible equipment deficiency due to a temporary circuit configuration.

Procedural Deficiency

The Reactor Instrument Line Excess Flow Valve surveillance procedure (N2-ISP-ISC-ROOI) does account for the possibility of pressure surges within the instrument lines (as a result of this test procedure) causing an inadvertent instrument trip. But, it does not require checks for a possible tripped device or require resetting the trip signal.

Equipment Deficiency

At the time this surveillance procedure was being conducted various MSIV logic circuit modifications were being performed (as detailed in engineering and design coordination report M10074) which could have had a significant impact on this incident. In particular, the Instrument and Control (I&C) technicians performing this test stated that no MSIV channel trip indication annunciated, as it should have, when the first level transmitter tripped. It is possible that the circuit

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modifications being performed may have placed the circuit in a temporary configuration such that the trip indication would not have come in. Therefore, a temporary circuit configuration as described would have made a trip condition on the MSIV logic difficult to detect.

Further investigation into this matter (see Corrective Actions Taken) is being conducted.

III. ANALYSIS OF EVENT

There were no safety consequences to the plant or public as a result of this incident. Had this event occurred at 100% power, the transient that would result would be similar to and bounded by, the "Closure of all MSIV's" event discussed in FSAR section 15.2.4.

The duration of this incident from the first device trip to the MSIV isolation was approximately 3 1/2 hours.

- IV. CORRECTIVE ACTIONS TAKEN
- 1. Surveillance procedure N2-ISP-ISC-R001 will be revised prior to its next use to include checks for tripped devices and provisions for resetting trip signals.
- 2. The particular MSIV trip circuits and associated indication, involved in this incident, will be tested for proper performance. This test will be completed prior to initial criticality.

V. ADDITIONAL INFORMATION

No other LER's cover events similar to that discussed in this report

Identification of Components Referred to in this LER

Component	IEEE 803 EIIS Funct	IEEE 805 System ID
Main Steam Isolation Valves	ISV	JM
Logic Circuits	N/A	JM
Indicators	XI	JM
Excess Flow Check Valve	SHV	JM
Level Transmitter	LT	JE

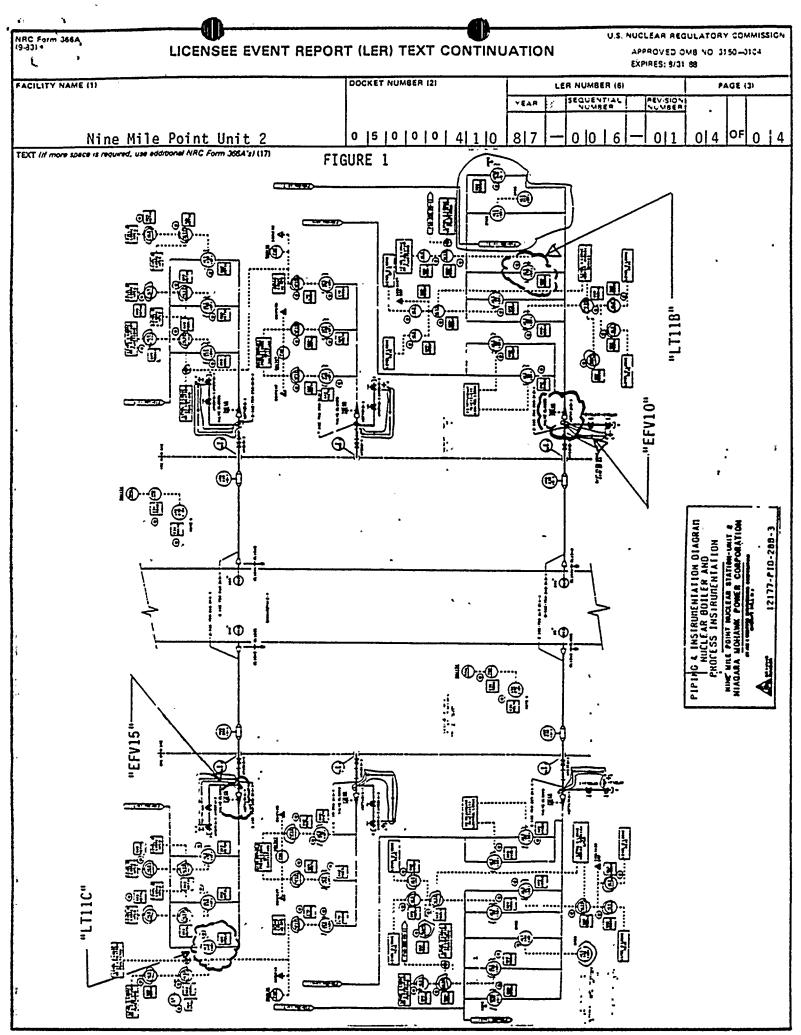
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