

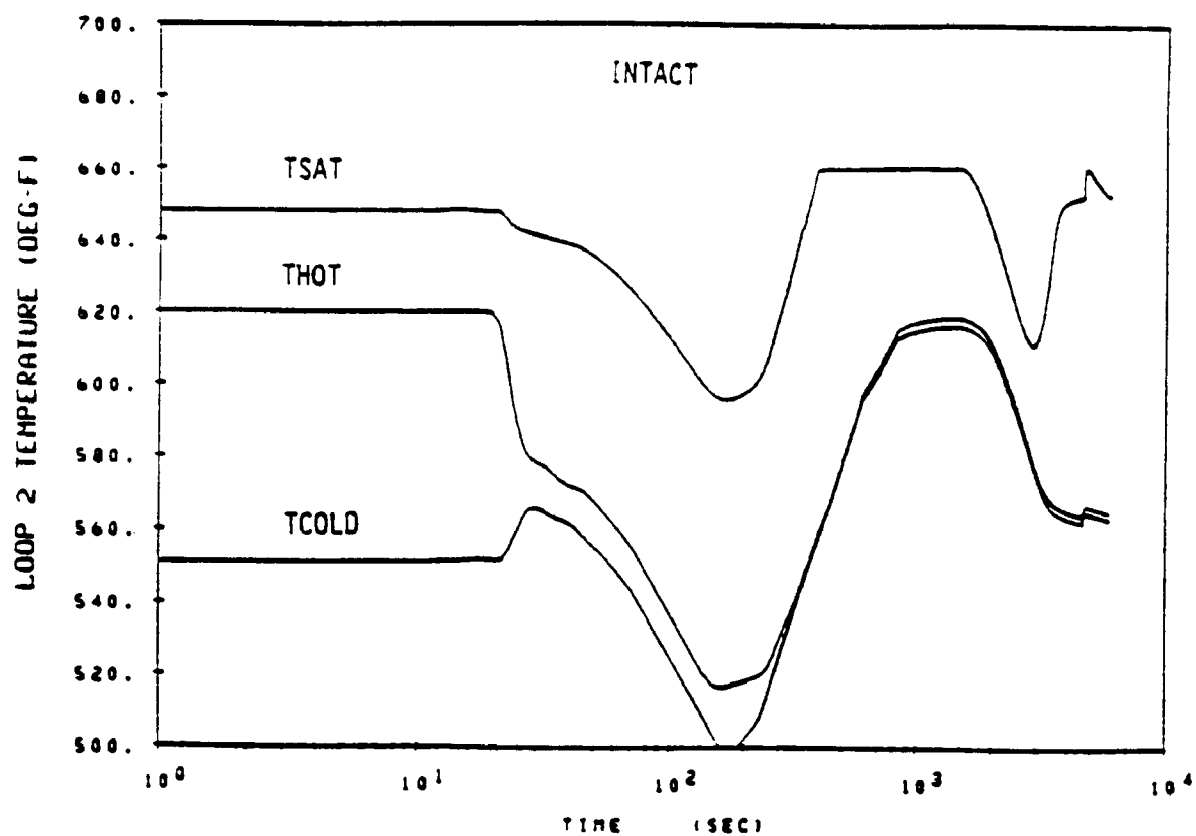
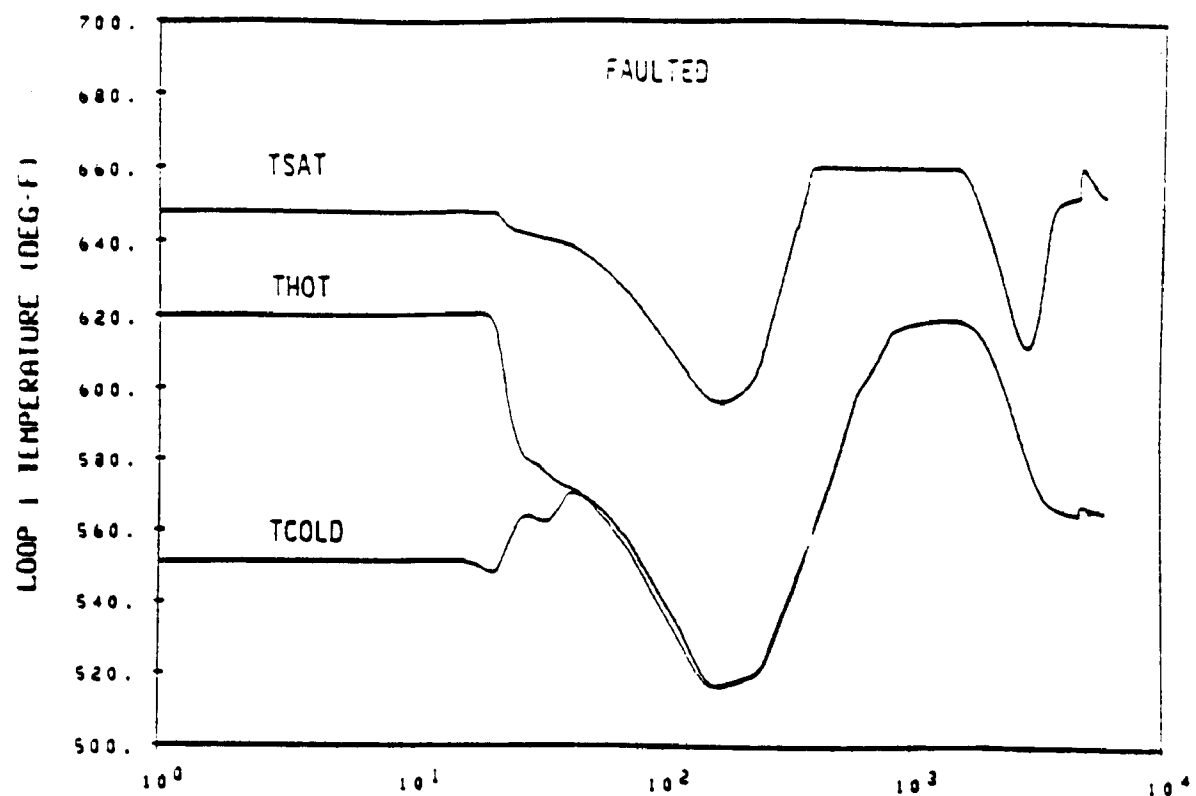


Figure 14.2.8-3 Feedline Break with Power
Faulted and Non-Faulted Loop Temperatures Versus Time