



PROJ0751

October 18, 2016
JEXU-1041-8528

Document Control Desk
U.S. Nuclear Regulatory Commission
11555 Rockville Pike
Rockville, MD 20852-2738

Attention: Mr. Joseph Holonich

SUBJECT: Submittal of MELTAC Topical Report Supporting Documentation

With this letter, Mitsubishi Electric Corporation (MELCO) submits the documents listed in the enclosures table below to the U.S. Nuclear Regulatory Commission (NRC).

Enclosed are the documents listed in the Table below.. As indicated in the enclosed materials, these documents contain information that MELCO considers proprietary, and therefore should be withheld from public disclosure pursuant to 10 C.F.R 2.390 (a)(4) as trade secrets and commercial or financial information which is privileged or confidential.

The proprietary information contained within these documents is extensive and non-proprietary versions of these documents will not be prepared. The accompanying affidavit, Enclosure (1) sets forth the basis on which the information identified as proprietary should be withheld from public disclosure.

Sincerely,

Shigeru Sugitani
Senior Manager, Control & Protection Systems Section
Nuclear Power Department, Energy Systems Center
Mitsubishi Electric Corporation

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Enclosures:

No.	Document Number	Document Title	Date of Issue
✓ 1	-	Affidavit of Shigeru Sugitani	10/12/2016
✓ 2	JEXU-1041-1126-P (Note)	MELTAC Platform CPU Module Circuit Block Level FMEA	10/18/2016
✓ 3	JEXU-1024-1071-P	CPU Module (PCPJ-31) CPUCNT_FPGA Specification	07/09/2014
✓ 4	JSX4L410-P	Input/ Output Function Program Specification	06/08/2012
✓ 5	JEXU-1035-7151-P	CPU Module (PCPJ-31) FPGA Specification V&V Task Report	08/11/2014
✓ 6	JEXU-1035-6025-P	Controller Software Specification V&V Task Report	06/01/2012
✓ 7	JEXU-1035-6030-P	Controller Program Specification V&V Task Report	06/14/2013
✓ 8	JEXU-1035-7251-P	CPU Module (PCPJ-31) Source Code V&V Task Report	11/21/2014
✓ 9	JEXU-1035-7351-P	CPU Module (PCPJ-31) FPGA Unit Test Task Report	12/25/2015
✓ 10	JEXU-1035-6071-P	Controller Source Code V&V Task Report	06/19/2013
✓ 11	JEXU-1035-6111-P	Controller Unit Test V&V Task Report	01/31/2014
✓ 12	JEXU-1028-1222-P	Testing Specification for Environmental Qualification Testing	07/03/2015
✓ 13	JEXU-1028-1223-P	Testing Specification for Seismic Qualification Testing	10/08/2015
✓ 14	JEXU-1028-1224-P	Testing Specification for Electrostatic Qualification Testing	08/26/2015
✓ 15	JEXU-1028-1225-P	Testing Specification for Electromagnetic Compatibility Qualification Testing	08/26/2015
✓ 16	JEXU-1028-1226-P	Testing Specification for Isolation Qualification Testing	10/17/2015
✓ 17	JEXU-1028-1236-P	Testing Specification for Environmental Qualification Testing (Isolation Module; Current Input Module)	07/24/2015
✓ 18	JEXU-1024-1021-P	CPU Module (PCPJ-31) Hardware Requirement Specification	05/14/2014
✓ 19	JEXU-1024-1051-P	CPU Module (PCPJ-31) H/W Specification	06/11/2015
✓ 20	JSX4L400-P	Controller Software Specification	04/16/2012

NOTE) JEXU-1041-1126-P, "MELTAC Platform CPU Module Circuit Block Level FMEA" has been prepared as the submittal document for JEXU-1041-1052," Circuit Block-level FMEA for CPU Module PCPJ-31", which has been described to submit in the Table 3 of JEXU-1041-2099 (R1),"Schedule for Providing the Responses to the Request for Additional Information".

CC: Gilbert W. Remley
 Nuclear Systems Department Manager
 Mitsubishi Electric Power Products, Inc

ENCLOSURE 1

MITSUBISHI ELECTRIC CORPORATION

AFFIDAVIT

I, Shigeru Sugitani, state as follows:

1. I am the Senior Manager of the Control & Protection Systems Section, Nuclear Power Department, of Mitsubishi Electric Corporation ("MELCO"), and have been delegated the function of reviewing MELCO's digital platform documentation to determine whether it contains information that should be withheld from public disclosure pursuant to 10 C.F.R. § 2.390 (a)(4) as trade secrets and commercial or financial information which is privileged or confidential.
2. In accordance with my responsibilities, I have reviewed the enclosed documents (refer to Table 1), and have determined that portions of the document contain proprietary information that should be withheld from public disclosure. Those pages containing proprietary information are identified with the label "Proprietary" on the top of the page and the proprietary information has been bracketed with an open and closed bracket as shown here "[]". The first page of the document indicates that all information identified as "Proprietary" should be withheld from public disclosure pursuant to 10 C.F.R. § 2.390 (a)(4).
3. The information identified as proprietary in the enclosed document has in the past been, and will continue to be, held in confidence by MELCO and its disclosure outside the company is limited to regulatory bodies, customers and potential customers, and their agents, suppliers, and licensees, and others with a legitimate need for the information, and is always subject to suitable measures to protect it from unauthorized use or disclosure.
4. The basis for holding the referenced information confidential is that it describes the hardware and software information for the digital platform "MELTAC", developed by MELCO and not used in the exact form by any of MELCO's competitors. This information was developed at significant cost to MELCO, since it required research development and detailed design for its software and hardware extending over several years.
5. The referenced information is being furnished to the Nuclear Regulatory Commission ("NRC") in confidence and solely for the purpose of information to the NRC staff.
6. The referenced information is not available in public sources and could not be gathered readily from other publicly available information. Other than through the provisions in paragraph 3 above, MELCO knows of no way the information could be lawfully acquired by organizations or individuals outside of MELCO.

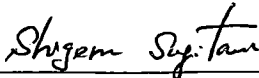
7. Public disclosure of the referenced information would assist competitors of MELCO in their design of nuclear power plants without incurring the costs or risks associated with the design and testing of the subject systems. Therefore, disclosure of the information contained in the referenced document would have the following negative impacts on the competitive position of MELCO in the nuclear plant market:
- A. Loss of competitive advantage due to the costs associated with development and testing for MELTAC. Providing public access to such information permits competitors to duplicate or mimic MELTAC design without incurring the associated costs.
 - B. Loss of competitive advantage created by benefits of enhanced plant safety, and reduced operation and maintenance costs associated with MELTAC, for nuclear power plants.

I declare under penalty of perjury that the foregoing affidavit and the matters stated therein are true and correct to the best of my knowledge, information and belief.

Table 1 Responses to request for additional information

No.	Document Title	Document Number	Rev.
1	MELTAC Platform CPU Module Circuit Block Level FMEA	JEXU-1041-1126-P	0
2	CPU Module (PCPJ-31) CPUCNT_FPGA Specification	JEXU-1024-1071	0
3	Input/ Output Function Program Specification	JSX4L410	0
4	CPU Module (PCPJ-31) FPGA Specification V&V Task Report	JEXU-1035-7151	0
5	Controller Software Specification V&V Task Report	JEXU-1035-6025	0
6	Controller Program Specification V&V Task Report	JEXU-1035-6030	0
7	CPU Module (PCPJ-31) Source Code V&V Task Report	JEXU-1035-7251	0
8	CPU Module (PCPJ-31) FPGA Unit Test Task Report	JEXU-1035-7351	0
9	Controller Source Code V&V Task Report	JEXU-1035-6071	0
10	Controller Unit Test V&V Task Report	JEXU-1035-6111	0
11	Testing Specification for Environmental Qualification Testing	JEXU-1028-1222	0
12	Testing Specification for Seismic Qualification Testing	JEXU-1028-1223	0
13	Testing Specification for Electrostatic Qualification Testing	JEXU-1028-1224	0
14	Testing Specification for Electromagnetic Compatibility Qualification Testing	JEXU-1028-1225	0
15	Testing Specification for Isolation Qualification Testing	JEXU-1028-1226	0
16	Testing Specification for Environmental Qualification Testing (Isolation Module; Current Input Module)	JEXU-1028-1236	0
17	CPU Module (PCPJ-31) Hardware Requirement Specification	JEXU-1024-1021	0
18	CPU Module (PCPJ-31) H/W Specification	JEXU-1024-1051	0
19	Controller Software Specification	JSX4L400	0

Executed on this 18th day of October, 2016.



Shigeru Sugitani
 Senior Manager, Control & Protection Systems Section
 Nuclear Power Department, Energy Systems Center
 Mitsubishi Electric Corporation

Please direct questions regarding this submittal to NRC, Joe Holonich (301) 415-7297 or

Ken Krayvo



KEN KRAYVO
SENIOR ENGINEER
NUCLEAR SYSTEMS DEPARTMENT

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