
RESPONSE TO REQUEST FOR ADDITIONAL INFORMATION

8/10/2016

**SAFETY SYSTEM DIGITAL PLATFORM
- MELTAC (MITSUBISHI ELECTRIC TOTAL ADVANCED CONTROLLER) -
TOPICAL REPORT**

Mitsubishi Electric Corporation

TAC NO.: MF4228
RAI NO.: #1
DATE OF RAI ISSUE: 6/29/2016

QUESTION NO.: 8 for JEXU-1041-1008, "Safety System Digital Platform – MELTAC"

P. 76, Section 4.1.7.2, Memory Integrity Check (MIC), describes an activity that is part of the verification program for the MIC software tool. Per ISG-06, Section D.10.4.2.3.2, the information needed for the NRC staff to reach a determination that the software tools are adequate for their intended use should be contained in the documentation for the software tool verification program. Therefore, provide the procedure for the verification of the MIC tool to demonstrate adequacy. In addition, please clarify the following: How far along is the development of the MIC? Is it currently in use by operating plants? Please explain if all of the system memory locations are checked during this activity including data locations and unused memory.

ANSWER:

ISG-06, Section D.10.4.2.3.2 refers to IEEE 7-4.3.2 which pertains to "software tools used to support software development processes and verification and validation (V&V) processes". [

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Impact on Topical Report

There is no impact on the Topical Report.