

ZONE/LTR	REVISIONS	DATE	APPROVED
	RELEASE ON C/M N 8130		
A	C/M N 8132 - REV'D. & UP-DATED	2-27-74	W.S.P.
B	C/M N 9270 - REV'D. & UP-DATED	9-16-76	W.S.P.
F	C/M N 94 - TESTPOINTS 'E' WERE 'T'. ADDED INPUT/OUTPUT TS & PC RECEPT PIN DESIGNATIONS. REVISED NOTES 4 & 5. DELETED NOTE 6 AND ADDED NEW NOTES 6 & 8. UPDATED TABLE I. ADDED - 'AND TDO OUTPUT SHOULD NOT BE USED' TO NOTE 2. <i>Access note 2</i>		

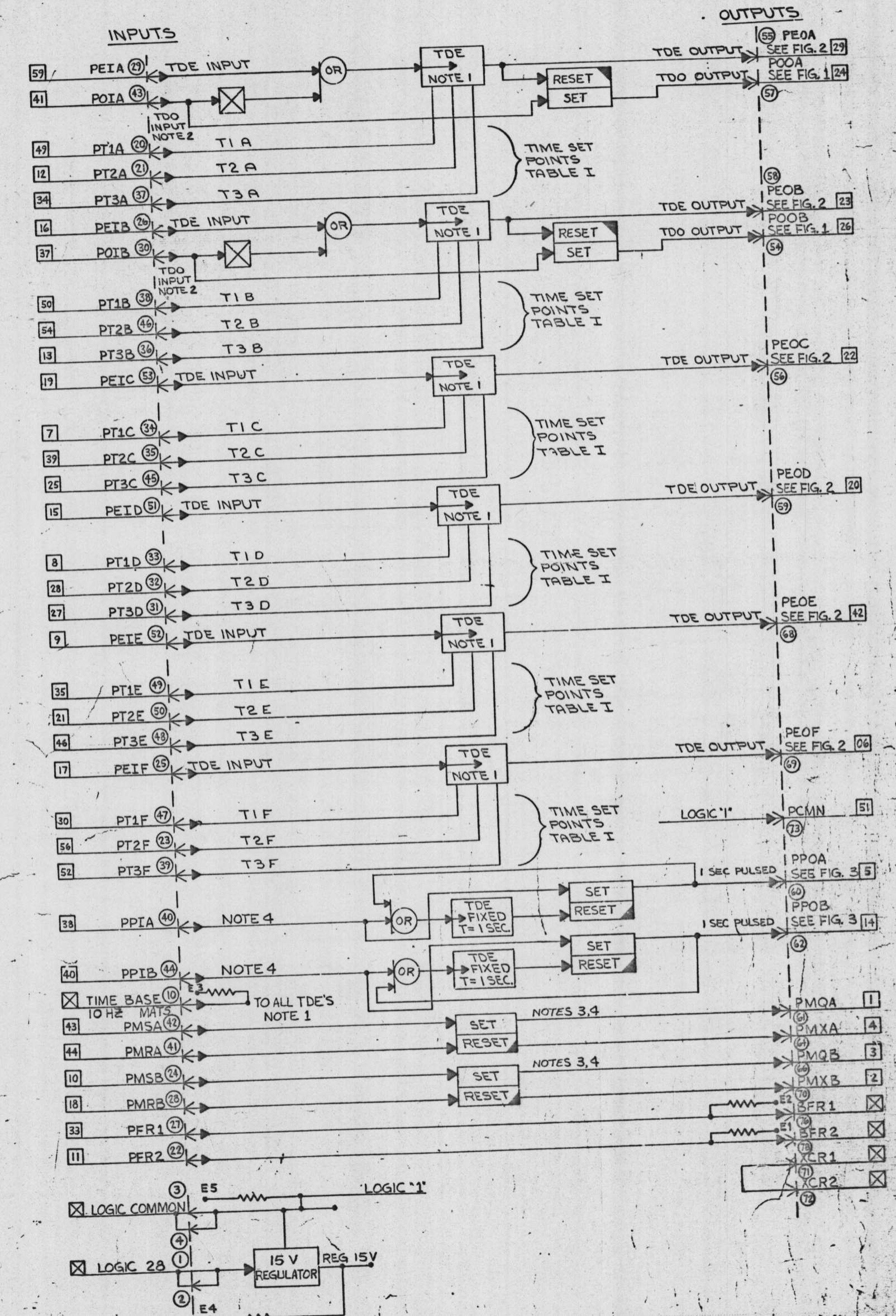
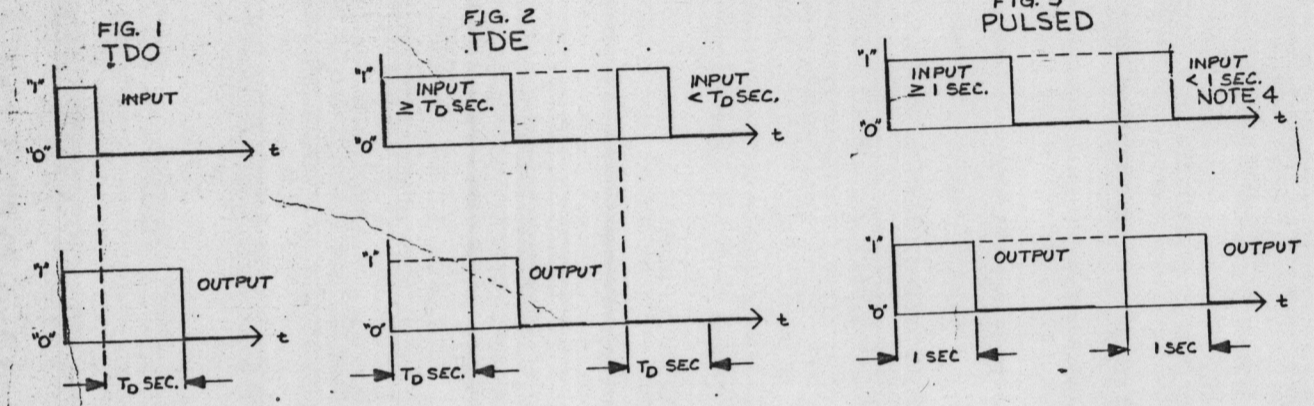


TABLE I

TIME DELAY IN SECONDS	T3	T2	T1
0	0	0	0
1	0	0	1
2	0	1	0
3	0	1	1
5	1	0	0
10	1	0	1
14.8	1	1	0
29.8	1	1	1



- NOTES:
- TIME DELAYS ARE DIGITAL COUNTERS DRIVEN BY A 10KHZ CLOCK LOCATED ON SEQ LOGIC CARD OF EACH CABINET. CLOCK HAS AN ACCURACY (INITIAL +0° TO 70°C TEMP. DRIFT) OF ±.0035%. CLOCK DRIFT IS ±.0005% PER YEAR. IF CLOCK SHOULD FAIL SYSTEM WILL AUTOMATICALLY SWITCH OVER TO AN ALTERNATE CRYSTAL CLOCK WHICH IS IDENTICAL WITH NORMAL CLOCK.
  - WHEN TDE MODE IS USED, TDO INPUT MUST BE CONNECTED TO LOGIC '1' AND TDO OUTPUT SHOULD NOT BE USED.
  - WHEN SET AND RESET INPUTS ARE BOTH '1', SET OUTPUT IS '0' AND RESET OUTPUT IS '1' REGARDLESS OF PRIOR OUTPUT.
  - ALL TDM CIRCUITS USING MEMORIES (MEM, TDO, PULSE) BEHAVE THUS: A SET INPUT CAUSES AN IMMEDIATE SET OUTPUT (IN THE ABSENCE OF RESET); HOWEVER - THE SET INPUT WILL NEVER LATCH FOR INPUTS LESS THAN 3 MILLISECONDS. THE SET INPUT WILL ALWAYS LATCH FOR INPUTS LONGER THAN 60 MILLISECONDS. THE SET INPUT MAY OR MAY NOT LATCH FOR INPUTS BETWEEN 3 AND 60 MILLISECONDS.
  - 'E' DESIGNATES TEST POINT AVAILABLE ON CARD FRONT EDGE WHEN CABINET FRONT DOOR IS OPENED. TEST POINTS ARE ISOLATED WITH RESISTORS.
  - NUMBERS SHOWN THUS [ ] ARE PROGRAM PANEL TERMINAL BLOCK TERMINAL NUMBERS. NUMBERS SHOWN THUS ( ) ARE PRINTED CIRCUIT RECEPTACLE PIN NUMBERS. SIGNALS NOT AVAILABLE ON PROGRAM PANEL ARE SHOWN THUS [X].
  - WHENEVER POWER IS LOST & RESTORED, MEMORIES COME ON IN RESET STATE & TDE ELEMENTS COME ON RESET.
  - UNUSED INPUTS BEHAVE AS LOGIC '0'.
  - ALL OUTPUTS ARE SUPPLIED BY TDM CIRCUITS.

APERTURE CARD

APPROVED  
MAR 13 1977

PROJECT: BELLEVILLE NO. 1  
CONTRACT: JSR 3-15550  
DRAWING NO. KFW 2316  
REVISION: 1 REV. C UNIT 152

POOR ORIGINAL

QTY	ITEM NO.	NOMENCLATURE OR DESCRIPTION	DWG. SIZE	PART OR IDENTIFYING NO.	SPECIFICATION	MATERIAL OR NOTE	REMARKS
LIST OF MATERIAL							
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES X .XX .XXX ANGLES 2.00 2.01 2.00 2.12" FRACTIONS & ALL MACH. SURFACES ✓ RNS DRAUGHTING PER INDUSTRY STANDARDS & SHARP EDGES PER JE1215 45° CHAMFER FIRST THREAD TELEGRAPHIC PER ANSI Y17.5 GOAL FOR APPLY TO STOCK SIZES							
CONSOLIDATED CONTROLS CORPORATION BETHLEHEM, PA.				LOGIC DIAGRAM TIME DELAY & MEMORY MODULE (TDM)			
MATERIAL: 15V REGULATOR		FOR BELLEVILLE NUCLEAR PLANT, UNIT 1 & 2		SIZE CODE IDENT NO. F 02750		KFW 2316	
DRAWN: R. QUERY		CHECKED: J. J. JARRETT 10-3-76		SCALE: 1/8" = 1"		SHEET 1 OF 1	

Docket # 50-438  
Contract # 820208004  
Date 8-20-2022 of Document REGULATORY DOCKET FILE